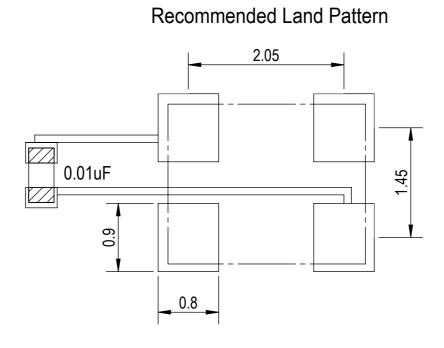
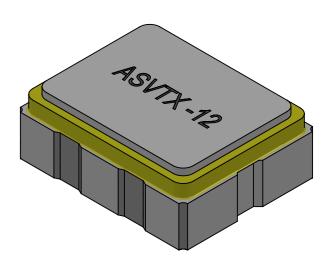


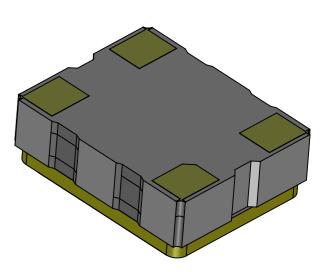
PIN	FUNCTION					
FIIN	VCTCXO	TCXO				
1	Vcc	GND				
2	GND					
3	OUTPUT					
4	Vdd					





Note: Recommended using an approximately 0.01uF bypass capacitor between PIN 2 and 4.





UNLESS OTHERWISE SPECIFIED: FINISH: DIMENSIONS ARE IN MM		DEBUR AND BREAK SHARP		DO NOT SCALE DRAWING		REVISION	REVISION					
SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:						EDGES	ABRACON					
	NAME	SIGNATURE	DATE				CORPORATION					
DRAWN	XXXXXX						20222 Fanaranza Danaha Canta margarita California 02600					
CHK'D	XXXXXX						30332 Esperanza, Rancho Santa margarita, California 92688					
APPV'D							TITLE: TEMPERATURE COMPENSATED/ VOLTAGE CONTROLLED CRYSTAL OSCILLATOR					
MFG												
Q.A				MATERIAL:			ASVTX-12			A3		
							<i>F</i>	- ∠				
				WEIGHT:			SCALE:40:1		SHEET 1 OF 1	-		