6

PAMIRA

\*03

Date Code

10

White dot

indicates

Pin 1

02/23/00 MP

at 1168kbps rates

- A. Electrical Specifications (@ 25°C)
  - 1. Primary (Line side) Impedance;  $135\Omega$
  - 2. Secondary (IC side) Impedance;  $42\Omega$
  - 3. Longitudinal Balance; 53dBm MIN @ 500Hz TO 292KHz, 0dBm (Per FCC part 68.310, pin 7 grounded)
  - 4. Insertion Loss; 1.0dB MAX @ 100KHz, 0dBm
  - 5. Total Harmonic Distortion; -84.5dB TYP @ 5KHz, 1.45Vrms
  - 6. Primary Open Circuit Inductance; 0.60mH ±5% @ 10KHz, 1.0Vrms Measured (9-7)

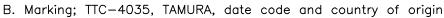
HDSL transformer designed for use with Level One HDSL IC chips SK70704/SK70707

- Leakage Inductance; 20 $\mu$ H MAX @ 100KHz, 0.1Vrms Measured (9-7) with 2 & 4, and 1 & 5 shorted
- 8. Interwinding Capacitance; 75pF MAX @ 100KHz, 1.0Vrms Measured (1-9) with 2 & 4 shorted

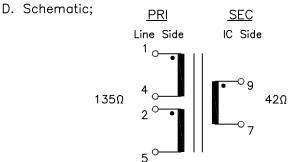
 $(7-9)=3.23\Omega \pm 10\%$ 

- 9. DC Resistance;  $(1-4)=3.46\Omega \pm 10\%$  $(2-5)=3.46\Omega \pm 10\%$
- 10. Turns Ratio;  $(1-4):(2-5)=1:1.00\pm2\%$  $(7-9):(1-5)=1:1.80\pm2\%$

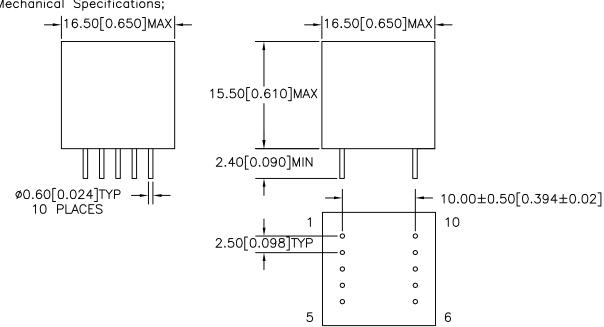
2 & 4 shorted 11. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec



- C. Safety; Complies with UL1950 3rd Edition and IEC950



E. Mechanical Specifications;



PREPARED BY:

D. Rund

D. Italia			
ENGINEER:	DWG_CONTROL_NO. REV	HDSL	
M. Pitchai	P-A1-12358 ACAD\TTC\A1123581.DWG PRE	TRANSFORMER	TTC-4035
QUALITY CONTROL:	ICONTENTS OF THIS DRAWING AR	TAMURA CORPORATION OF AMERICA	MODEL SPECIFICATION
D. Kelley	PRIOR NOTICE	T 43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624 (909) 699-1270 FAX 9096769482	DIM: mm(In) SCL: 2/1 SH: 1 0F 1
APPROVED:		PRINT OR DOCUMENT AND SUBJECT MATTER DISCLOSED HE	

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