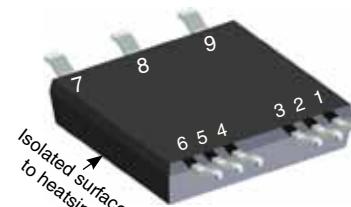
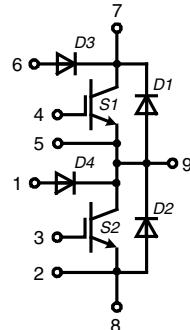


XPT IGBT phaseleg

ISOPLUS™

Surface Mount Power Device

I_{C25} = 32 A
V_{CES} = 1200 V
V_{CE(sat)} typ = 1.8 V



E72873

IGBTs S1, S2

Symbol	Conditions	Maximum Ratings		
V _{CES}	T _{VJ} = 25°C to 150°C	1200	V	
V _{GES}		±20	V	
I _{C25}	T _C = 25°C	32	A	
I _{C80}	T _C = 80°C	23	A	
I _{CM}	V _{GE} = 15 V; R _G = 56 Ω; T _{VJ} = 125°C	45	A	
V _{CEK}	RBSOA, clamped inductive load; L = 100 μH	V _{CES}		
t _{sc} (SCSOA)	V _{CE} = 900 V; V _{GE} = ±15 V; R _G = 56 Ω; T _{VJ} = 125°C none repetitive	10	μs	
P _{tot}	T _C = 25°C	130	W	

Symbol **Conditions****Characteristic Values**(T_{VJ} = 25°C, unless otherwise specified)

		min.	typ.	max.	
V _{CE(sat)}	I _C = 15 A; V _{GE} = 15 V; T _{VJ} = 25°C T _{VJ} = 125°C		1.8 2.1	2.1	V
V _{GE(th)}	I _C = 0.6 mA; V _{GE} = V _{CE}	5.4		6.5	V
I _{CES}	V _{CE} = V _{CES} ; V _{GE} = 0 V; T _{VJ} = 25°C T _{VJ} = 125°C		250	125	μA
I _{GES}	V _{CE} = 0 V; V _{GE} = ± 20 V			500	nA
t _{d(on)} t _r t _{d(off)} t _f E _{on} E _{off} E _{(rec)off}	Inductive load; T _{VJ} = 125°C V _{CE} = 600 V; I _C = 15 A V _{GE} = ±15 V; R _G = 56 Ω		70 40 250 100 1.55 1.7		ns ns ns ns mJ mJ
C _{ies} Q _{Gon}	V _{CE} = 25 V; V _{GE} = 0 V; f = 1 MHz V _{CE} = 600 V; V _{GE} = 15 V; I _C = 15 A		tbd 48		pF nC
R _{thJC} R _{thJH}	with heatsink compound (IXYS test setup)		1.35	1.0 1.7	K/W K/W

Features**• XPT IGBT**

- low saturation voltage
- positive temperature coefficient for easy paralleling
- fast switching
- short tail current for optimized performance in resonant circuits

• Sonic™ diode

- fast reverse recovery
- low operating forward voltage
- low leakage current

• V_{CEsat} detection diode

- integrated into package
- very fast diode

• Package

- isolated back surface
- low coupling capacity between pins and heatsink
- PCB space saving
- enlarged creepage towards heatsink
- application friendly pinout
- low inductive current path
- high reliability

Applications**• Phaseleg**

- buck-boost chopper

• Full bridge

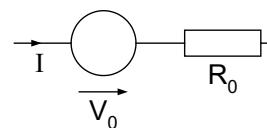
- power supplies
- induction heating
- four quadrant DC drives
- controlled rectifier

• Three phase bridge

- AC drives
- controlled rectifier

Diodes D1, D2

Symbol	Conditions	Maximum Ratings		
I _{F25}	T _C = 25°C		27	A
I _{F80}	T _C = 80°C		18	A
Symbol	Conditions	Characteristic Values		
		(T _{VJ} = 25°C, unless otherwise specified)		
		min.	typ.	max.
V _F	I _F = 20 A	T _{VJ} = 25°C	2.0	2.3
		T _{VJ} = 125°C	2.0	V
I _{RM} t _{rr} E _{rec}	I _F = 20 A; R _G = 56 Ω; T _{VJ} = 125°C V _R = 600 V; V _{GE} = -15 V		20 350 tbd	A ns mJ
R _{thJC}	per diode		1.35	K/W
R _{thJH}	with heatsink compound (IXYS test setup)		1.75	K/W

Equivalent Circuits for Simulation**Conduction**

IGBTs (typ. at V_{GE} = 15 V; T_J = 125°C)
S1, S2 V_o = 1.1 V; R_o = 90 mΩ

Diodes (typ. at T_J = 125°C)
D1, D2 V_o = 1.3 V; R_o = 41 mΩ

Diodes D3, D4

Symbol	Conditions	Maximum Ratings		
V _R	T _C = 25°C to 150°C		1200	V
Symbol	Conditions	Characteristic Values		
		(T _{VJ} = 25°C, unless otherwise specified)		
		min.	typ.	max.
V _F	I _F = 1 A	T _{VJ} = 25°C	1.7	2.2
		T _{VJ} = 125°C	1.5	V
I _R	V _R = 1200 V	T _{VJ} = 25°C		2
		T _{VJ} = 125°C	30	μA
I _{RM} t _{rr}	I _F = 1 A; di _F /dt = -100 A/μs; T _{VJ} = 25°C V _R = 100 V; V _{GE} = 0 V		2.3 40	A ns

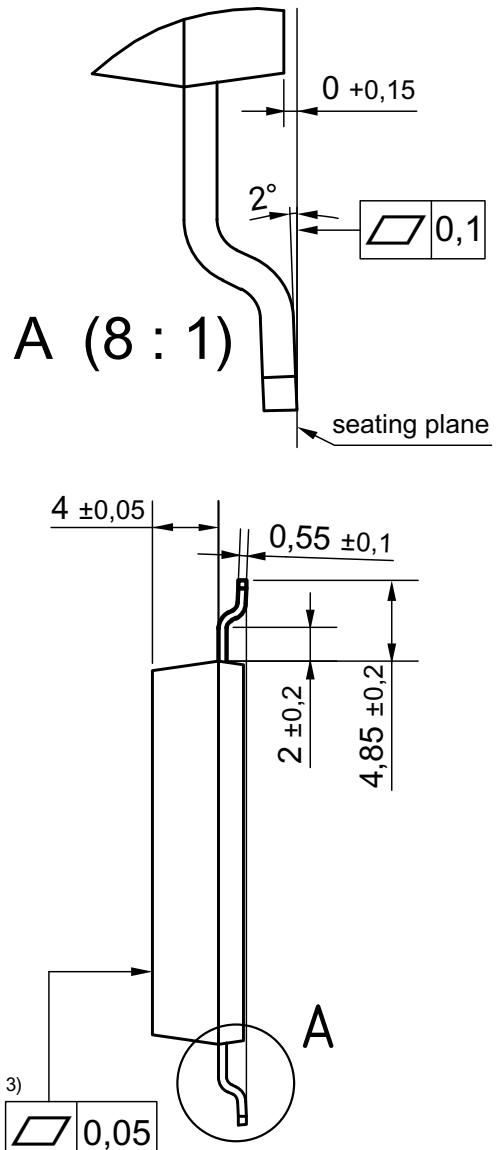
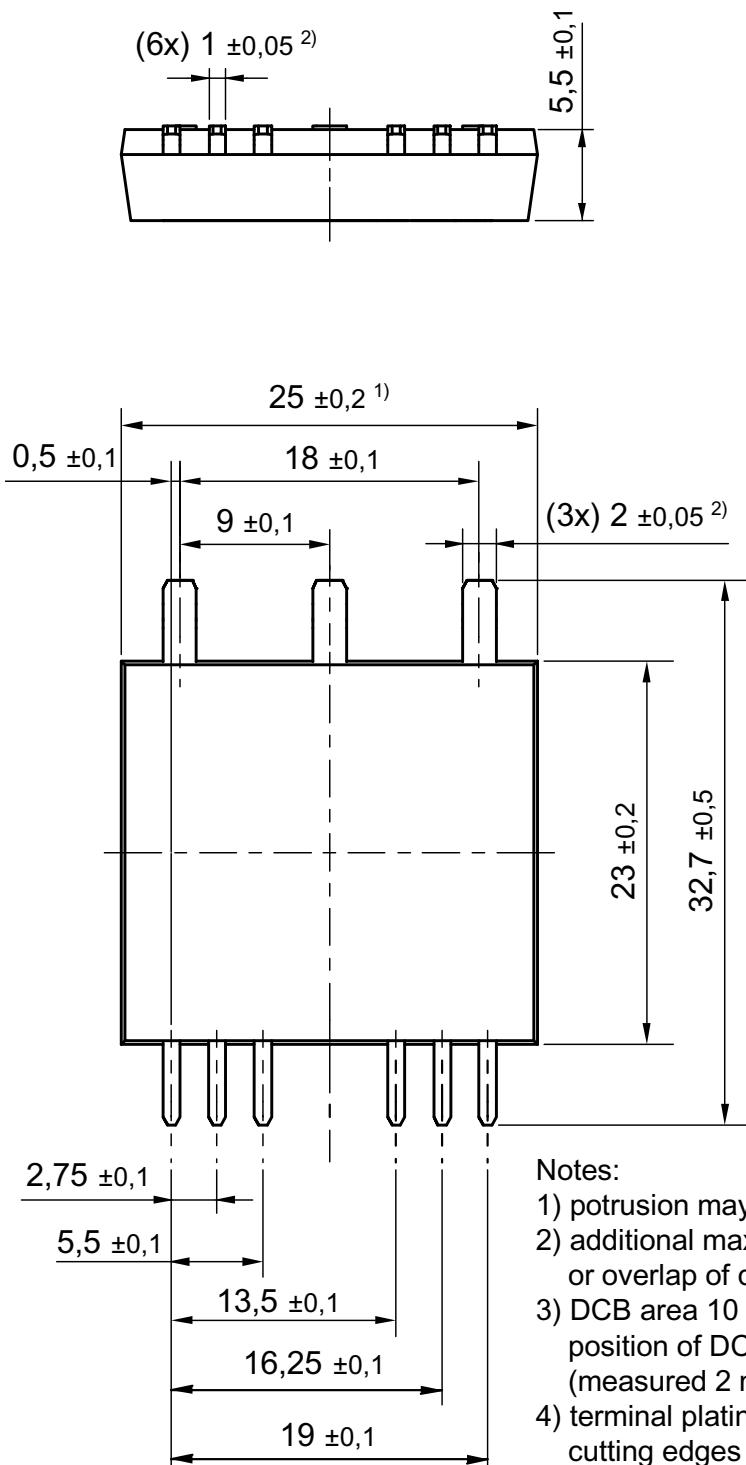
Component

Symbol	Conditions	Maximum Ratings		
T _{VJ}		-55...+150	°C	
T _{stg}		-55...+125	°C	
V _{ISOL}	I _{ISOL} ≤ 1 mA; 50/60 Hz	2500	V~	
F _c	mounting force	40 ... 130	N	

Symbol	Conditions	Characteristic Values		
		min.	typ.	max.
C _P	coupling capacity between shorted pins and backside metal		90	pF
d _S , d _A	pin - pin	1.65		mm
d _S , d _A	pin - backside metal	4		mm
CTI		400		
Weight			8	g

Ordering	Ordering Name	Marking on Product	Delivering Mode	Base Qty	Ordering Code
Standard	IXA20PG1200DHGLB	IXA20PG1200DHGLB	Tape&Reel	200	tbd

Dimensions in mm (1 mm = 0.0394")



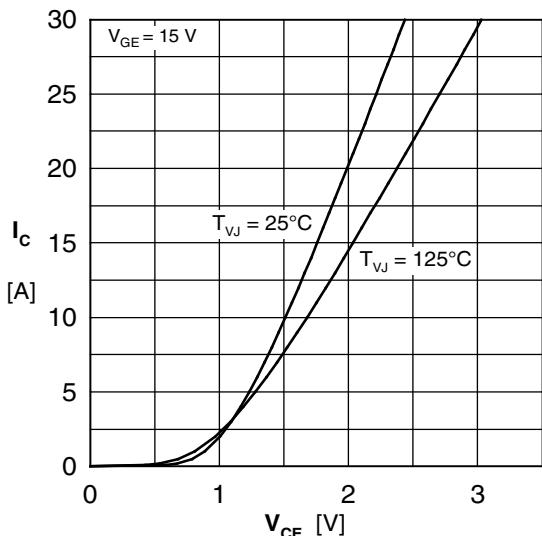


Fig. 1 Typ. output characteristics

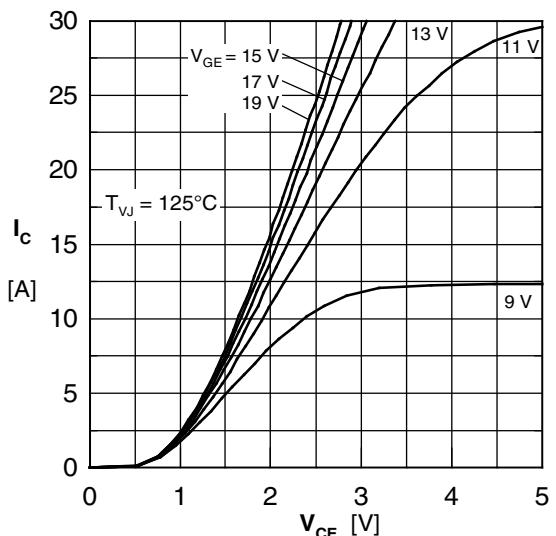


Fig. 2 Typ. output characteristics

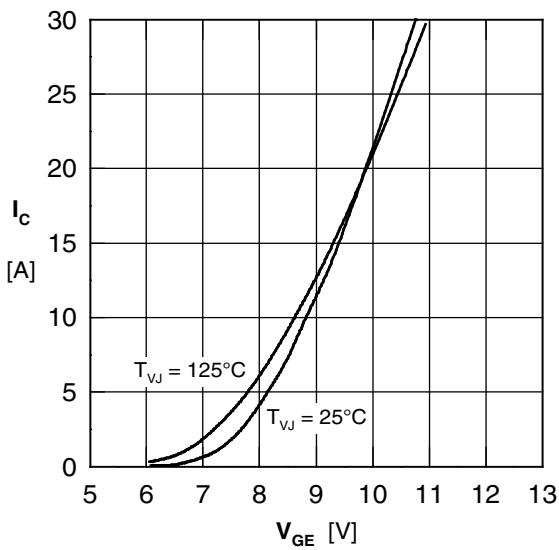


Fig. 3 Typ. tranfer characteristics

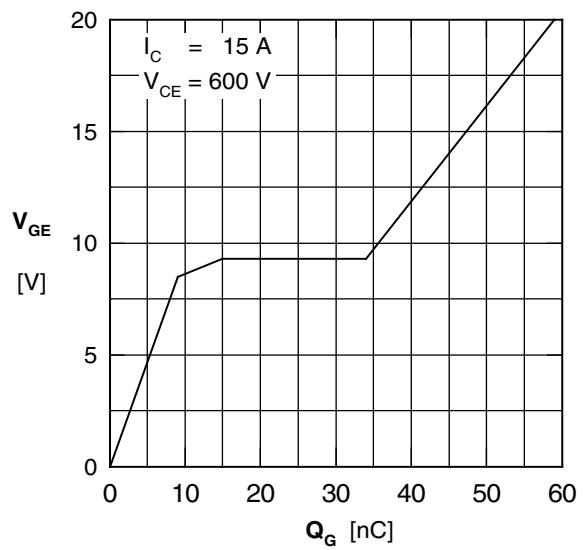


Fig. 4 Typ. turn-on gate charge

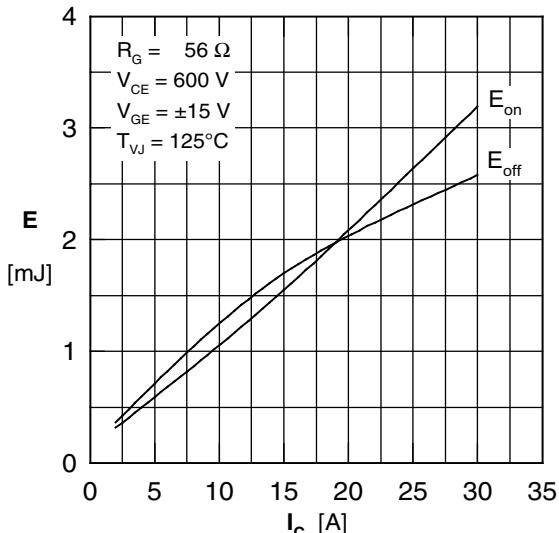


Fig. 5 Typ. switching energy vs. collector current

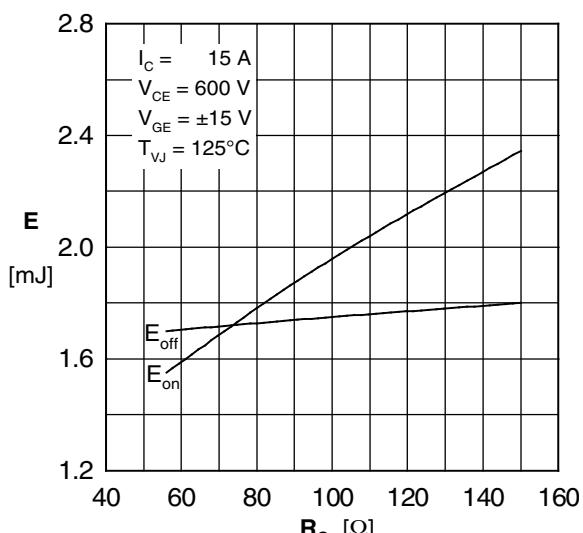


Fig. 6 Typ. switching energy vs. gate resistance

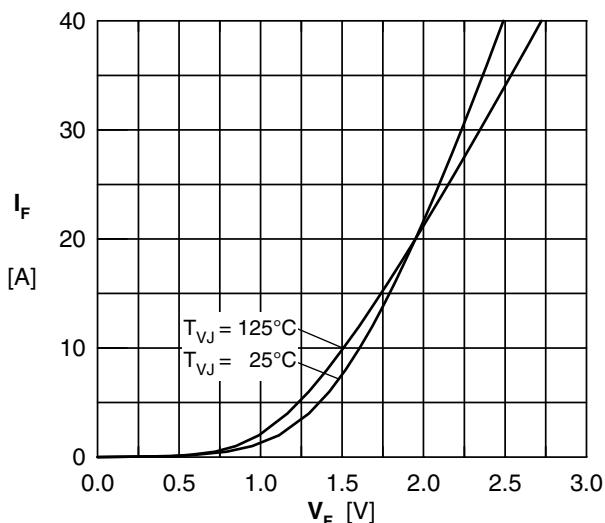
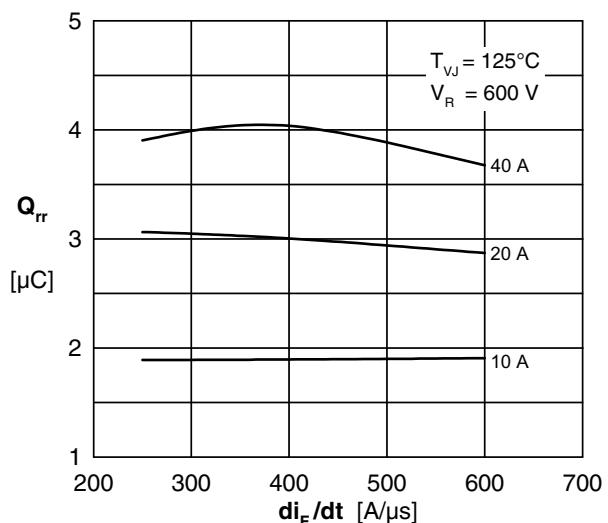
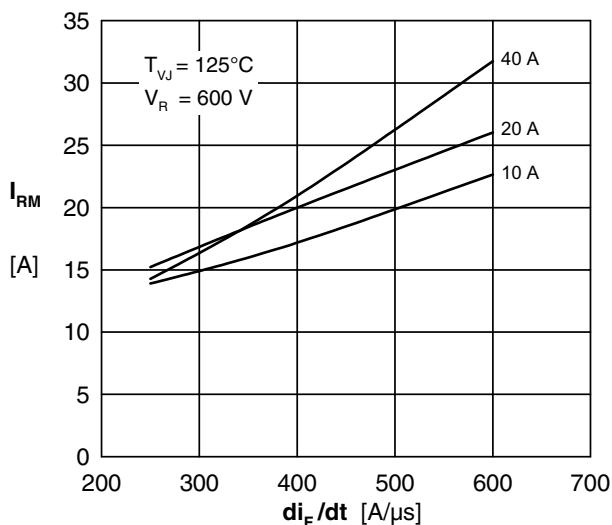
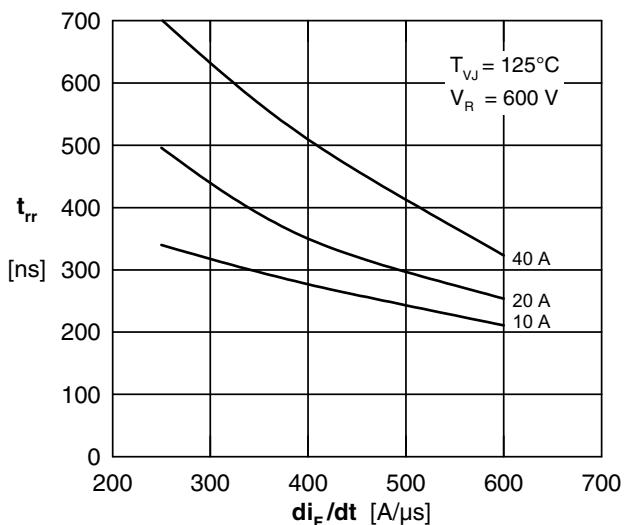
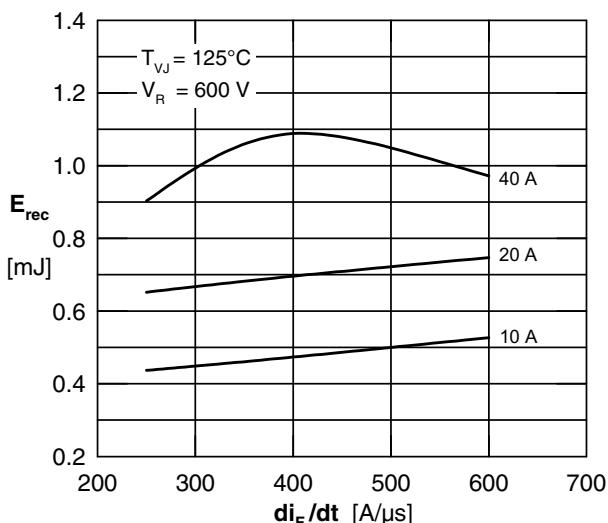
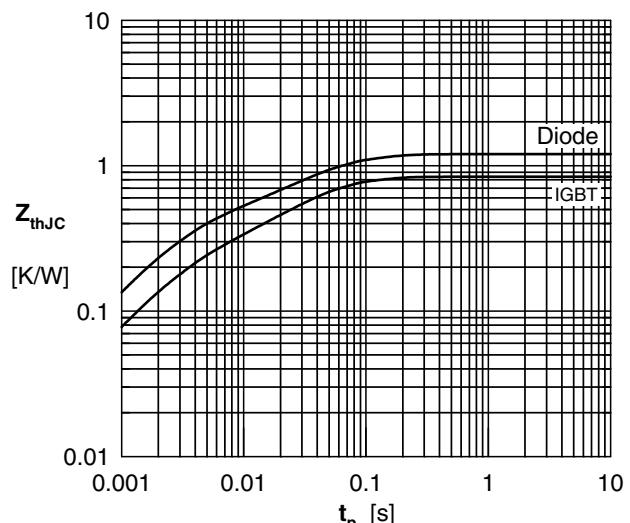
Fig. 7 Typ. Forward current versus V_F Fig. 8 Typ. reverse recov.charge Q_{rr} vs. di/dt Fig. 9 Typ. peak reverse current I_{RM} vs. di/dt Fig. 10 Typ. recovery time t_{rr} versus di/dt Fig. 11 Typ. recovery energy E_{rec} versus di/dt 

Fig. 12 Typ. transient thermal impedance



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