HI1812T800R-10

UNCONTROLLED **DOCUMENT**

NATURAL COOLING

PHYSICAL DIMENSIONS:

IMPEDANCE

20

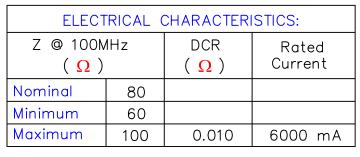
A 4.50 [.177] ± 0.25 [.010]

B 3.20 [.126] ± 0.25 [.010]

C 1.40 [.055] ± 0.25 [.010]

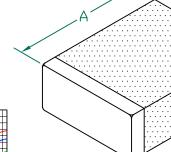
D 0.65 [.026] ± 0.45 [.018]

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



NOTES: UNLESS OTHERWISE SPECIFIED

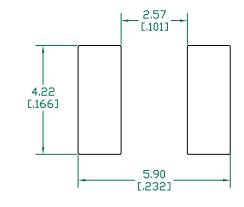
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2500 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)



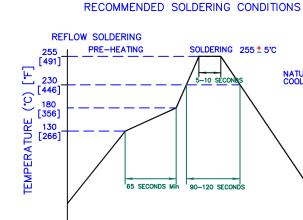


FREQUENCY (MHz)

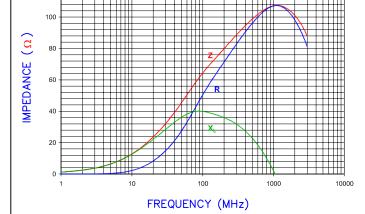
|Z| , R, AND X vs. FREQUENCY



LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension)



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		DIMENSIONS ARE IN mm [INCHE	This print is the property of Laird							
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					rights to design or invention are reserved.	•				
	D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	Т	REV	PART TO	PE:	DRAWN BY:
ı		UPDATE LAIRD LOGO AND REFLOW CURVE			HI1812T800R-10	- 1	D	CO-FIRE		ТМВ
	С	UPDATE D DIMENSION	03/12/10	JUN	1111012100011 10	- 1				'
	В	UPDATE COMPANY LOGO ADD ROHS	01/24/08	JRK	DATE: 04/02/04	SCA	LE: N	TS	SHEET:	
	Α	ORIGINAL DRAFT	04/02/04	ТМВ	CAD #	TOO		-	2	of 2
	REV	DESCRIPTION	DATE	INT	HI1812T800R-10-D				01 2	

AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3298