

1.0MHZ BOOST CONVERTER WITH 38V INTERNAL MOSFET SWITCH

May 2013

GENERAL DESCRIPTION

IS31BL3508A is a boost converter featuring an integrated MOSFET switch designed for driving series strings of LED or other device. Input operating voltage range is 2.7V~5.5V. The device regulates the LED load current is set using an external, low value sensing resistor. The device adjusts the PWM duty cycle of the switch to maintain the voltage at the FB pin at a value of 0.3V.

The IS31BL3508A operates at a constant switching frequency of 1.0MHz to allow for small component size. IS31BL3508A features an over voltage shutdown pin which prevents the output voltage from exceeding 38V (typ.) in the case of an open circuit condition.

The device is offered in a TSOT-23-6 and SOT-23-6 package and a temperature rating from -40° C $\sim +85^{\circ}$ C.

FEATURES

- Supply voltage: 2.7V~5.5V
- Built-in soft start
- Drive series strings of 10 LEDs or 3×8 LEDs
- PWM or DC voltage dimming
- Frequency of PWM is 500Hz ~ 500kHz
- 1.0MHz high speed switching frequency
- 0.3V feedback voltage
- Integrated high power MOSFET
- Over voltage protection
- -40°C ~ +85°C working temperature range
- TSOT-23-6 and SOT-23-6 package

APPLICATIONS

- Small LCD Backlights
- Notebook PC
- GPS

TYPICAL APPLICATION CIRCUIT

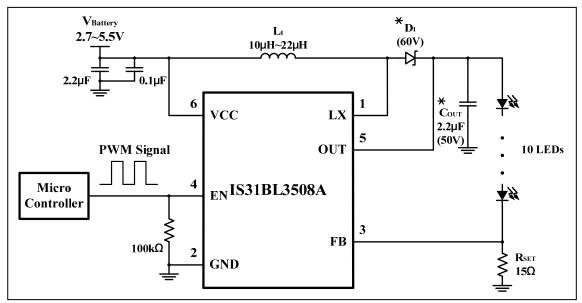


Figure 1 Constant current white LED driver using PWM at the EN pin to control intensity

Note 1: C_{OUT} must be rated for 50V. Smaller values of output capacitance will cause the LED current ripple to increase.

Note 2: The resistor R_{EST} should be placed as close as possible to the FB pin to prevent the EMI.

Note 3: D₁ must choose 60V and peak current is determined by the load. Please refer to Page 9 for detail information.



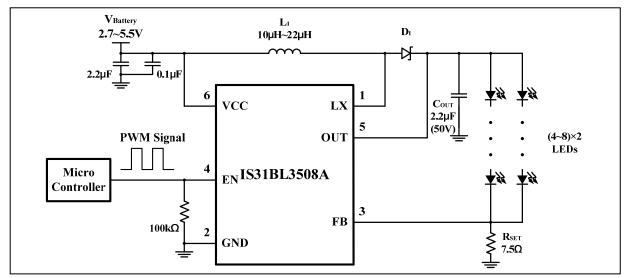


Figure 2 Constant current white LED driver using PWM at the EN pin to control intensity (4~8)×2 LEDs

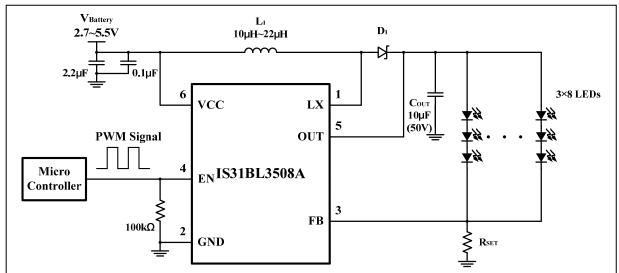


Figure 3 Constant current white LED driver using PWM at the EN pin to control intensity 3×8 LEDs

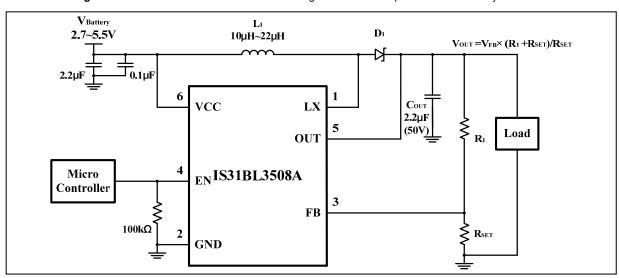


Figure 4 Constant output voltage for general purpose use



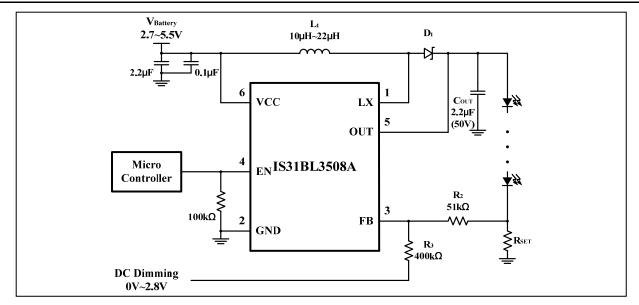


Figure 5 Constant output current with DC dimming input

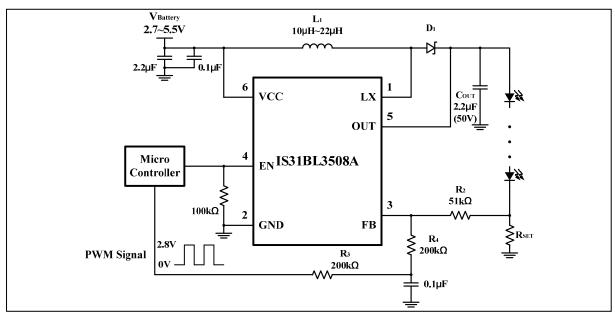


Figure 6 Constant output current with PWM dimming control

IS31BL3508A



PIN CONFIGURATION

Package	Pin Configurations (Top View)		
TSOT-23-6 SOT-23-6	LX		

PIN DESCRIPTION

No.	Pin	Description	
1	LX	Switch input, connect to the inductor.	
2	GND	Ground.	
3	FB	Feedback input pin.	
4	EN	Device enable pin, active high.	
5	OUT	Output voltage sense pin for over voltage protection.	
6	VCC	Supply voltage input pin.	





ORDERING INFORMATION Industrial Range: -40°C to +85°C

Order Part No.	Package	QTY/Reel	
IS31BL3508A-TTLS2-TR	TSOT-23-6, Lead-free	2000	
IS31BL3508A-STLS2-TR	SOT-23-6, Lead-free	3000	

Copyright © 2013 Lumissil Microsystems. All rights reserved. Lumissil Microsystems reserves the right to make changes to this specification and its products at any time without notice. Lumissil Microsystems assumes no liability arising out of the application or use of any information, products or services described herein. Customers are advised to obtain the latest version of this device specification before relying on any published information and before placing orders for products.

Lumissil Microsystems does not recommend the use of any of its products in life support applications where the failure or malfunction of the product can reasonably be expected to cause failure of the life support system or to significantly affect its safety or effectiveness. Products are not authorized for use in such applications unless Lumissil Microsystems receives written assurance to its satisfaction, that:

- a.) the risk of injury or damage has been minimized;
- b.) the user assume all such risks; and
- c.) potential liability of Lumissil Microsystems is adequately protected under the circumstances





ABSOLUTE MAXIMUM RATINGS

Supply voltage, V _{CC}	-0.3V ~ +6.0V
LX pin voltage	-0.3V ~ +40V
Voltage at any input pin	-0.3V ~ +6.0V
Maximum junction temperature, T _{JMAX}	+150°C
Storage temperature range, T _{STG}	-65°C ~ +150°C
Operating temperature range, T _A	-40°C ~ +85°C

Note: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

Test conditions: $T_A = 25$ °C, $V_{CC} = 3.6$ V, unless otherwise noted.

Symbol	Parameter	Condition	Min	Тур	Max	Unit
V _{CC}	Supply voltage		2.7		5.5	V
I_{CC}	Quiescent current	Switching		1.1	1.3	mA
I _{SD}	Shutdown current	V _{EN} = 0V		2	2.5	μA
$V_{EN_{ON}}$	EN input logic high	V _{EN} rising	1.4			V
V _{EN_OFF}	EN input logic low	V _{EN} falling			0.4	V
f _{osc}	Operating frequency			1		MHz
D_{MAX}	Maximum duty cycle		91.5	92.7	95.5	%
R _{DS_ON}	LX switch on resistance			0.7		Ω
I _{SW_LK}	LS switch leakage current	V _{SW} = 38V			1	μA
I _{SW_LIMIT}	Switch over current limit	Duty = 90%	1.2	1.6	1.8	Α
V _{OVP}	OUT over voltage protection		36	38	40	V
V_{FB}	Feedback voltage		285	300	315	mV
T _{ST}	Over temperature protection	(Note 1)		160		°C

Note 1: Guaranteed by design.



TYPICAL PERFORMANCE CHARACTERISTIC

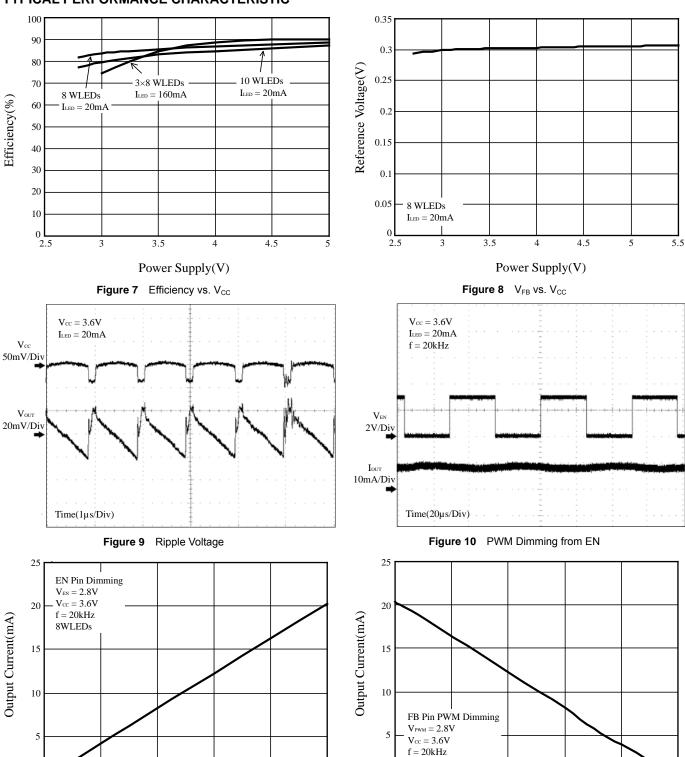


Figure 11 Enable Pin Dimming

Duty Cycle(%)

Figure 12 FB pin PWM Dimming

Duty Cycle(%)

8WLEDs

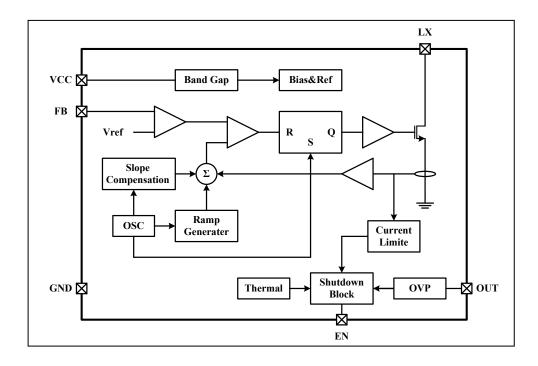
20

0

100



FUNCTIONAL BLOCK DIAGRAM





APPLICATION INFORMATION

INPUT AND OUTPUT CAPACITOR

A 2.2µF input capacitor is used to reduce input ripple and noise, where the input ripple amplitude is inversely proportional to the value of the input capacitance. The input and output capacitors should be placed as close to the device as possible, so as to reduce the effect of voltage ripple. The value of the output capacitor is dependent upon the output ripple. A 2.2µF or 10µF ceramic capacitor with a 50V rating is sufficient for the majority of applications.

INDUCTOR VALUE

The choice of the inductor value will affect the performance of the device. Large value inductors will reduce the current ripple in the inductor, thus reducing output voltage ripple. However large value inductors of the same current rating, typically also have larger DC resistance, thus reducing the system efficiency. For example in Figure 1, the loads are 10 WLEDs and output current is 20mA. The maximum input current can reach 600mA. For this application a 10µH 700mA inductor is sufficient.

DIODE

The diode, D_1 , should use schottky diode to optimize the efficiency of system. The rated current and peak current of D_1 should be above output current and peak current of L_1 . And the recurrent peak reverse voltage of D_1 should be above OVP voltage. A 1A 60V schottky diode is recommended.

LED CURRENT SETPOINT

When the IS31BL3508A is used for LED driving applications (Figure 1), the LED current flows through the external feedback resistor R_{SET} to regulate the LED current. The internal feedback regulation point is 0.3V. The maximum LED current may be calculated by the following Equation (1).

$$I_{MAX} = V_{FB} / R_{SET} \tag{1}$$

For accurate LED current, resistors should be 1% or better tolerance.

LED INTENSITY CONTROL

EN PIN DIMMING

The EN pin of the IS31BL3508A can accept a PWM signal to implement LED dimming (Figure 1). LED current may be computed using the following Equation (2).

$$I_{IFD} = I_{MAX} \cdot D_{PWM} \tag{2}$$

 I_{MAX} is computed using equation (1). Recommended PWM frequency is in the range of 500Hz ~ 500kHz. So as to guarantee a reasonably good dimming effect, the minimum duty cycle should be about 10%.

Driving the EN pin with a PWM signal can effectively adjust the LED intensity. The PWM signal voltage levels must meet the EN pin input voltage levels, $V_{\text{EN ON}}$ and $V_{\text{EN OFF}}.$

IS31BL3508A can also use a DC voltage or PWM signal to directly control the LED current, and thus provide fine adjustment for the LED intensity.

FB PIN DC VOLTAGE DIMMING

The schematic of Figure 5 show an example of using a DC input voltage at the FB pin to implement LED dimming. When the DC input voltage increases, the current through R_2 and R_3 increases producing an offset voltage across R_2 , thereby causing the LED current to be reduced. The LED current may be computed using the following Equation (3). V_{FB} is 0.3V (typ).

$$I_{LED} = \frac{V_{FB} - \frac{R_2 \times (V_{DC} - V_{FB})}{R_3}}{R_{SET}}$$
(3)

When the DC voltage range is $0V\sim1.8V$, a good value for R_2 is $75k\Omega$, and for R_3 is $360k\Omega$.

FB PIN PWM DIMMING

A PWM signal connected to the FB pin can also be used to directly control the LED current (Figure 6). A low pass filter converts the PWM duty cycle to a DC level, which is in turn summed together with the feedback voltage of RSET to effectively reduce the LED current proportionally. Due to the filtering of the PWM signal, the frequency of the PWM signal can have an affect on the output voltage ripple. For a PWM signal input signal of 2.8V, the recommended frequency is greater than 2kHz. Using a fixed frequency PWM signal and adjusting the PWM duty cycle effectively adjusts the LED intensity. LED current may be computed using the following Equation (4). V_{FB} is 0.3V (typ).

$$I_{LED} = \frac{V_{FB} - \frac{R_2 \times (V_{PWM} \times Duty - V_{FB})}{R_4 + R_3}}{R_{SET}} \tag{4}$$

For a PWM signal range of 0V~1.8V, a suitable value for R_2 is 75k Ω , R_3 is 150k Ω , R_4 is 200k Ω .

SETTING THE OUTPUT VOLTAGE

The IS31BL3508A can be configured to provide a constant output voltage (Figure 4). The output voltage level can be computed using the following Equation (5). The device internal V_{FB} is 0.3V (typ).

$$V_{OUT} = V_{FB} \times (R_1 + R_{SET}) / R_{SET}$$
 (5)

IS31BL3508A



OVERVOLTAGE THRESHOLD

Device open circuit protection is realized using the overvoltage protection function (OVP). If the output voltage surpasses the overvoltage threshold, the overvoltage protection circuit is activated and the device will stop working.





CLASSIFICATION REFLOW PROFILES

Profile Feature	Pb-Free Assembly	
Preheat & Soak Temperature min (Tsmin) Temperature max (Tsmax) Time (Tsmin to Tsmax) (ts)	150°C 200°C 60-120 seconds	
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	
Liquidous temperature (TL) Time at liquidous (tL)	217°C 60-150 seconds	
Peak package body temperature (Tp)*	Max 260°C	
Time (tp)** within 5°C of the specified classification temperature (Tc)	Max 30 seconds	
Average ramp-down rate (Tp to Tsmax)	6°C/second max.	
Time 25°C to peak temperature	8 minutes max.	

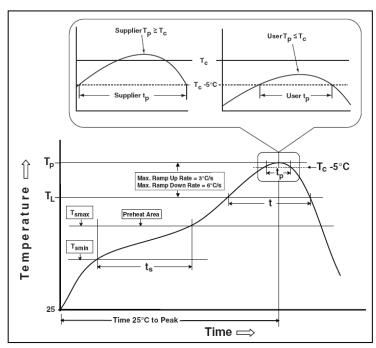
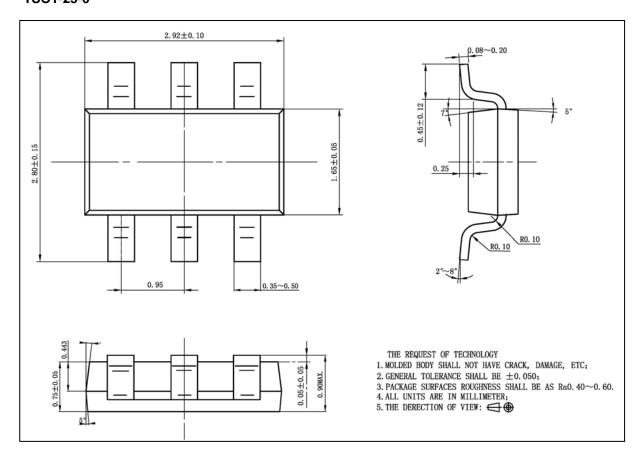


Figure 13 Classification Profile



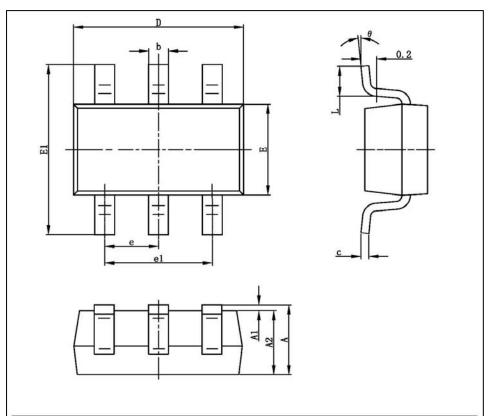
PACKAGE OUTLINE DRAWING

TSOT-23-6





SOT-23-6



Cb . 1	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Note: All dimensions in millimeters unless otherwise stated.