

#### **DATA SHEET**

# SKY12323-303LF: 0.5-3.0 GHz Five-Bit Digital Attenuator (1 dB LSB)

# **Applications**

- Transceiver transmit automatic level control or receive automatic gain control in GSM, CDMA, WCDMA, WLAN, Bluetooth<sup>®</sup>, or Zigbee<sup>®</sup> land mobile radio base stations or terminal equipment
- General-purpose signal attenuation in telecommunications and instrumentation applications

#### **Features**

• Broadband operation: 0.5 to 3.0 GHz

Attenuation range: 31 dBLSB attenuation: 1 dB

Low insertion loss: 1.4 dB @ 900 MHz
Positive voltage operation: 2.7 to 5.5 V
Low current consumption: <100 μA @ 3 V</li>

• High IIP3: +45 dBm

 Small, MSOP-10 (10-pin, 3 x 3 mm) package with exposed paddle (MSL1, 260 °C per JEDEC J-STD-020)



Skyworks Pb-free products are compliant with all applicable legislation. For additional information, refer to *Skyworks Definition of Lead (Pb)-Free*, document number SQ04-0073.

# **Description**

The SKY12323-303LF is a monolithic, GaAs binary-weighted five-bit single positive control voltage digital attenuator. The device operates from 0.5 to 3.0 GHz. DC power consumption is very low, typically 100  $\mu\text{A}$  maximum, with a control and supply voltage of 3 V.

The SKY12323-303LF has an LSB of 1 dB and total attenuation of 31 dB. The two RF ports are bilateral; each can be used as the RF input or the RF output. An external supply voltage of 2.7 to 5.5 V is required.

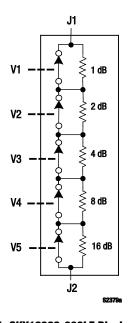


Figure 1. SKY12323-303LF Block Diagram

The SKY12323-303LF is comprised of five cascaded fixed attenuators, each of which has a shunt bypass switch. Beginning at the 1 dB LSB, each succeeding fixed attenuator produces twice the attenuation of the preceding stage.

The state of each bypass switch is controlled by the logic level voltage applied to the associated control voltage input. A logic high voltage closes the associated switch, which bypasses that fixed attenuator stage; a logic low opens the switch to force the input signal to that stage through the associated attenuator.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

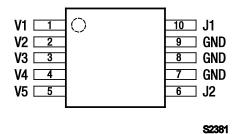


Figure 2. SKY12323-303LF Pinout – 10-Pin MSOP-10 (Top View)

**Table 1. SKY12323-303LF Signal Descriptions** 

| Pin# | Name | Description   | Pin # | Name | Description   |
|------|------|---|-------|------|---|
| 1    | V1   | High impedance control voltage input for 1 dB weighted bit (LSB)  | 6     | J2   | RF input or output port, supply voltage input if not supplied to pin 10 |
| 2    | V2   | High impedance control voltage input for 2 dB weighted bit        | 7     | GND  | Ground  |
| 3    | V3   | High impedance control voltage input for 4 dB weighted bit        | 8     | GND  | Ground  |
| 4    | V4   | High impedance control voltage input for 8 dB weighted bit        | 9     | GND  | Ground  |
| 5    | V5   | High impedance control voltage input for 16 dB weighted bit (MSB) | 10    | J1   | RF input or output port, supply voltage input if not supplied to pin 6  |

# **Electrical and Mechanical Specifications**

The absolute maximum ratings of the SKY12323-303LF are provided in Table 2. Electrical specifications are provided in Table 3.

Typical performance characteristics of the SKY12323-303LF are illustrated in Figures 3 through 6.

The state of the SKY12323-303LF is determined by the logic provided in Table 4.

**Table 2. SKY12323-303LF Absolute Maximum Ratings** 

| Parameter   | Symbol | Minimum | Typical | Maximum    | Units      |
|---|--------|---------|---------|------------|------------|
| RF input power (Vctl = 0/8 V): $f < 500 \text{ MHz}$ $f \geq 500 \text{ MHz}$ | Pin    |         |         | +30<br>+35 | dBm<br>dBm |
| Supply voltage  | Vs     | -0.2    |         | +0.2       | V          |
| Control voltage   | Vctl   | -0.2    |         | +8         | V          |
| Operating temperature   | Тор    | -40     |         | +85        | °C         |
| Storage temperature   | Тѕтс   | -65     |         | +150       | °C         |

**Note:** Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

**CAUTION**: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SKY12323-303LF Electrical Specifications (Note 1) (VcTL = 0/3 V, ToP = +25 °C, PIN = 0 dBm, Characteristic Impedance [Zo] = 50  $\Omega$ , Unless Otherwise Noted)

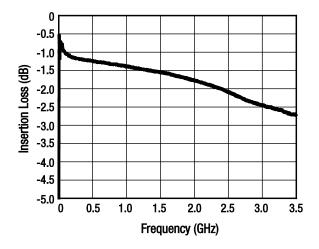
| Parameter                                   | Symbol     | Test Condition                         | Min  | Typical | Max      | Units |
|---|------------|--|--|---------|----------|-------|
| Insertion loss                              | IL         | 0.5 to 1.0 GHz                         |  | 1.4     | 2.0      | dB    |
|   |            | 1.0 to 2.0 GHz                         |  | 1.5     | 2.5      | dB    |
|   |            | 2.0 to 3.0 GHz                         |  | 2.3     | 3.0      | dB    |
| Attenuation range (Note 2)                  |            |  |  | 31      |          | dB    |
| Attenuation accuracy (Note 2)               |            | 0.5 to 3.0 GHz                         | $\pm (0.3 + 5\% \text{ of attenuation setting})$ |         |          | dB    |
| Return loss                                 | RL         | 0.5 to 3.0 GHz                         | 7  |         |          | dB    |
| Switching characteristics:                  |            |  |  |         |          |       |
| Rise/fall time                              |            | 10/90% or 90/10% RF                    |  | 150     |          | ns    |
| On/off time<br>Video feedthrough            |            | 50% VCTL to 90/10% RF<br>Trise = 1 ns. |  | 300     |          | ns    |
| video leedililougii                         |            | bandwidth = 500 MHz                    |  | 70      |          | mV    |
| Input power for 1 dB compression            | IP1dB      | 0.9 to 2.4 GHz:                        |  | 70      |          | 1117  |
| input power for 1 db compression            | IFTUD      |  |  |         |          |       |
|   |            | $V_{CTL} = 0 \text{ to } 3 \text{ V},$ |  | +30     |          | dBm   |
|   |            | $V_{CTL} = 0 \text{ to } 5 \text{ V}$  |  | +33     |          | dBm   |
| 3 <sup>rd</sup> Order Input Intercept Point | IIP3       | For two-tone input power,              |  |         |          |       |
|   |            | +5 dBm/tone, $\Delta f = 1$ MHz,       |  |         |          |       |
|   |            | 0.9 to 2.4 GHz:                        |  |         |          |       |
|   |            | $V_{CTL} = 0 \text{ to } 3 \text{ V},$ |  | +41     |          | dBm   |
|   |            | $V_{CTL} = 0 \text{ to } 5 \text{ V}$  |  | +45     |          | dBm   |
| Thermal resistance                          | Θις        |  |  | 25      |          | °C/W  |
| Supply voltage                              | <b>V</b> s |  | 2.8  | 3.0     | 8.0      | V     |
| Control voltage:                            |            |  |  |         |          |       |
| Low   | VLow       |  | -0.2   |         | +0.2     | V     |
| High  | VHIGH      |  | Vs - 0.2   |         | Vs + 0.2 | V     |
| Control port current                        | lctl       | Vctl = 0 V                             |  |         | 20       | μΑ    |
|   |            | Vctl = 3 V                             |  |         | 100      | μΑ    |
|   |            | <b>V</b> стL = 5 <b>V</b>              |  |         | 200      | μΑ    |

Note 1: Performance is guaranteed only under the conditions listed in this Table.

 $\textbf{Note 2:} \quad \textbf{Attenuation referenced to insertion loss.}$ 

# **Typical Performance Characteristics**

(Vs = 5 V, VcTL = 0 to 3 V, ToP = +25 °C, CBLK = 39 pF, PIN = 0 dBm, Characteristic Impedance [Zo] = 50  $\Omega$ , Unless Otherwise Noted)



**Figure 3. Insertion Loss vs Frequency** 

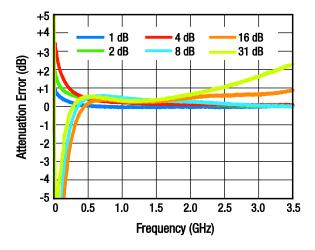


Figure 5. Attenuation Accuracy vs Frequency

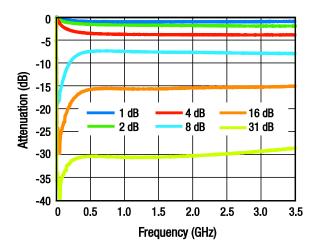


Figure 4. Attenuation vs Frequency (Normalized to Insertion Loss)

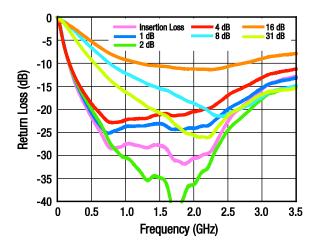


Figure 6. Return Loss vs Frequency

Table 4. SKY12323-303LF Truth Table

| J1 to J2<br>Attenuation  | V1<br>(Pin 1) | V2<br>(Pin 2) | V3<br>(Pin 3) | V4<br>(Pin 4) | V5<br>(Pin 5) |
|--------------------------|---------------|---------------|---------------|---------------|---------------|
| Reference insertion loss | VHIGH         | VHIGH         | VHIGH         | VHIGH         | VHIGH         |
| 1 dB                     | VLow          | VHIGH         | VHIGH         | VHIGH         | VHIGH         |
| 2 dB                     | VHIGH         | VLow          | VHIGH         | VHIGH         | VHIGH         |
| 4 dB                     | Vhigh         | VHIGH         | VLow          | VHIGH         | VHIGH         |
| 8 dB                     | Vhigh         | VHIGH         | VHIGH         | <b>V</b> Low  | VHIGH         |
| 16 dB                    | VHIGH         | VHIGH         | VHIGH         | VHIGH         | VLow          |
| 31 dB                    | VLow          | VLow          | VLow          | VLow          | VLow          |

**lote**: VHIGH = +3 V to 5 V. VLOW = 0 V to +0.2 V.

This Table shows the logic required for the major bits and full attenuation. Bit states need to be used in combination to set the sum of the bits selected.

## **Evaluation Board Description**

The SKY12323-303LF Evaluation Board is used to test the performance of the SKY12323-303LF digital attenuator. An assembly drawing for the Evaluation Board is shown in Figure 7 and an Evaluation Board schematic diagram is shown in Figure 8.

# **Package Dimensions**

Package dimensions for the 10-pin MSOP-10 are shown in Figure 9, and tape and reel dimensions are provided in Figure 10.

## **Package and Handling Information**

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

THE SKY12323-303LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

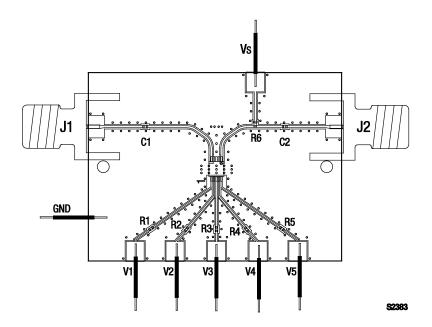


Figure 7. SKY12323-303LF Evaluation Board Assembly Diagram

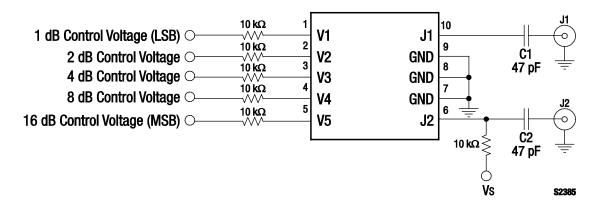


Figure 8. SKY12323-303LF Evaluation Board Schematic Diagram

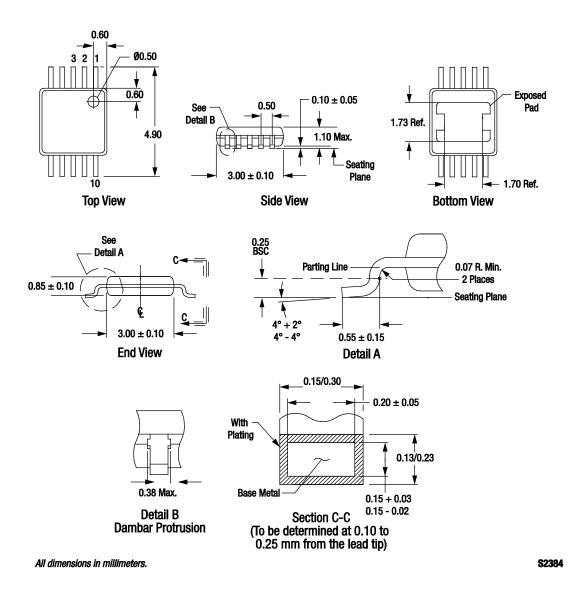


Figure 9. SKY12323-303LF 10-Pin MSOP-10 Package Dimensions

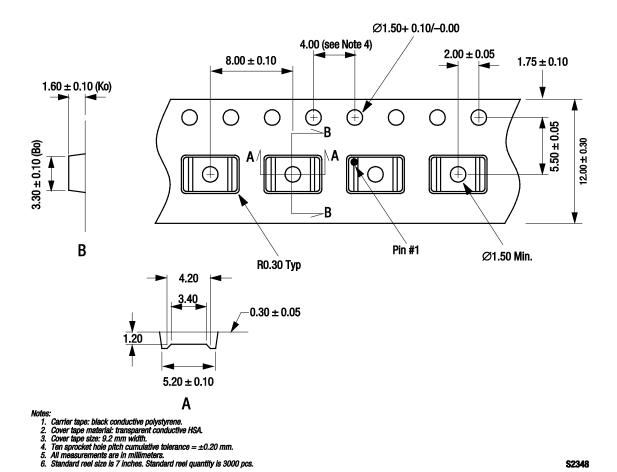


Figure 10. SKY12323-303LF Tape and Reel Dimensions

### **Ordering Information**

| Model Name                        | Manufacturing Part Number | <b>Evaluation Board Part Numbers</b> |
|-----------------------------------|---------------------------|--------------------------------------|
| SKY12323-303LF Digital Attenuator | SKY12323-303LF            | SKY12323-303LF-EVB                   |

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