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# FP1208 High frequency, high current power inductors



#### **Product features**

- 12.1x8.0x8.0mm maximum surface mount package
- Ferrite core material
- Controlled DCR for sensing circuits
- Inductance range from 150nH to 250nH
- Current range from 44 to 85 Amps
- Halogen free, lead free, RoHS compliant

#### Applications

- Multi-phase regulators
- Voltage Regulator Modules (VRMs)
- Desktop and server VRMs and EVRDs
- Data networking and storage systems
- · Graphics cards and battery power systems
- Point-of-Load modules
- DCR Sensing circuits

#### **Environmental data**

- Storage temperature range (component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant





12.3

Product Specifications								
Part	OCL 1	FLL min. <sup>2</sup>	I <sub>rms</sub> <sup>3</sup>	Isat 14	Isat 2 <sup>5</sup>	Isat 36	DCR	
Number <sup>8</sup>	(nH)±10%	(nH)	(Amps)	(Amps)	(Amps)	(Amps)	(mΩ) @ 20°C	K-factor 7
FP1208R1-R15-R	150	114		85	79	72		283
FP1208R1-R18-R	180	137		72	66	63		283
FP1208R1-R21-R	210	160	50	65	57	55	0.29±5%	283
FP1208R1-R23-R	230	176		61	53	50	]	283
FP1208R1-R25-R	250	191		55	48	44	]	283

1. Open Circuit Inductance (OCL) Test Parameters: 100kHz, 0.1Vrms, 0.0Adc@25°C

2. Full Load Inductance (FLL) Test Parameters: 100kHz, 0.1Vrms, Isat1

3. Irms: DC current for an approximate temperature rise of 40°C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed 125°C under worst case operating conditions verified in the end application.

4. Isat1: Peak current for approximately 20% rolloff @ 25°C

5. Isat2: Peak current for approximately 20% rolloff @ 85°C

6. Isat3: Peak current for approximately 20% rolloff @ 125°C

7. K-factor: Used to determine Bp-p for core loss (see graph). Bp-p = K  $^{\star}$  L \* ΔI \* 10<sup>-3</sup>. Bp-p:(Gauss), K: (K-factor from table), L: (Inductance in nH),  $\Delta I$  (Peak to peak ripple current in Amps).

8. Part Number Definition: FP1208Rx-Rxx-R:

- FP1208= Product code and size

- Rx= DCR indicator

- Rxx= Inductance value in µH

- "-R" suffix = RoHS compliant

## **Dimensions- mm**



Front View



DCR measured from point "a" to point "b"

Part marking: 1208Rx (Rx= DCR indicator), Rxx = Inductance value in uH (R= decimal point) wwllyy= date code, r= revision level

Tolerances are +/- 0.15 millimeters unless stated otherwise.

PCB tolerances are +/- 0.10 millimeters unless stated otherwise.

All soldering surfaces to be be coplanar within 0.1 millimeters.

### Packaging information - mm



Supplied in tape and reel packaging, 500 parts on a 13" diameter reel.

# FP1208 High frequency, high current power inductor

# Temperature rise vs total loss



# Core loss vs Bp-p



## Inductance characteristics



Table 1 Ctandard CpDb Calder (T.)

## **Solder Reflow Profile**



Table 1 - Star	ndard Shi	d SnPb Solder (1 <sub>C</sub> )		
	Volume	Volume		
Package	mm <sup>3</sup>	mm <sup>3</sup>		
Thickness	<350	≥350		
<2.5mm	235°C	220°C		
≥2.5mm	220°C	220°C		
Table 2 - Lea	d (Pb) Fre	e Solder (T <sub>C</sub> )		
Table 2 - Lea	d (Pb) Fre Volume	e Solder (T <sub>C</sub> ) Volume	Volume	
Table 2 - Lea Package			Volume mm <sup>3</sup>	
	Volume	Volume		
Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	mm <sup>3</sup>	
Package Thickness	<b>Volume</b> <b>mm</b> <sup>3</sup> <b>&lt;350</b> 260°C	Volume mm <sup>3</sup> 350 - 2000	mm <sup>3</sup> >2000	

## **Reference JDEC J-STD-020**

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak	<ul> <li>Temperature min. (T<sub>smin</sub>)</li> </ul>	100°C	150°C	
	<ul> <li>Temperature max. (T<sub>smax</sub>)</li> </ul>	150°C	200°C	
	<ul> <li>Time (T<sub>smin</sub> to T<sub>smax</sub>) (t<sub>s</sub>)</li> </ul>	60-120 Seconds	60-120 Seconds	
Average ramp up rate T <sub>smax</sub> to T <sub>p</sub>		3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperature (TL)		183°C	217°C	
Time at liquidous (t <sub>L</sub> )		60-150 Seconds	60-150 Seconds	
Peak package body temperature (T <sub>P</sub> )*		Table 1	Table 2	
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**	
Average ramp-down rate (Tp to Tsmax)		6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.	

 $^{\ast}$  Tolerance for peak profile temperature (T\_p) is defined as a supplier minimum and a user maximum.

\*\* Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.

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