

Low Frequency Transistor (20V, 3A)

2SD2150

Features

1) Low $V_{CE(sat)}$. $V_{CE(sat)} = 0.2V(Typ.)$

(Ic / IB = 2A / 0.1A)

- 2) Excellent current gain characteristics.
- 3) Complements the 2SB1424.

●Structure

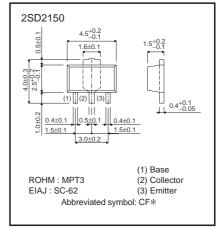
Epitaxial planar type NPN silicon transistor

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	40	V	
Collector-emitter voltage	VCEO	20	V	
Emitter-base voltage	VEBO	6	V	
Collector current	I-	3	A (DC)	
	lc lc	5	A (Pulse) *1	
Collector power dissipation	_	0.5	W	
	Pc	2	W *2	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

^{*1} Single pulse Pw=10ms

●Dimensions(Unit:mm)



* Denotes hre

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	40	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	20	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	6	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	_	0.1	μΑ	Vcb=30V
Emitter cutoff current	ІЕВО	_	_	0.1	μΑ	V _{EB} =5V
Collector-emitter saturation voltage	VCE(sat)	_	0.2	0.5	V	Ic/Iв=2A/0.1A *
DC current transfer ratio	hfe	120	_	560	_	Vce=2V, Ic=0.1A
Transition frequency	f⊤	_	290	_	MHz	Vce=2V, Ie= -0.5A, f=100MHz
Output capacitance	Cob	-	25	-	pF	Vce=10V, Ie=0A, f=1MHz

^{*} Measured using pulse current.

^{*2} Mounted on a 40×40×0.7mm Ceramic substrate.

2SD2150 Data Sheet

●Packaging specifications and hFE

		Package	Taping
		Code	T100
Туре	hfe	Basic ordering unit (pieces)	1000
2SD2150	RS		0

hre values are classified as follows:

Item	R	S
hfE	180 to 390	270 to 560

•Electrical characteristic curves

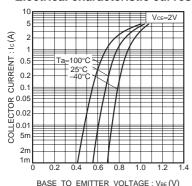


Fig.1 Grounded emitter propagation characteristics

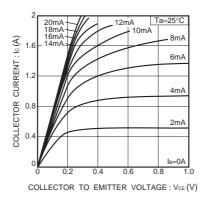


Fig.2 Grounded emitter output characteristics (I)

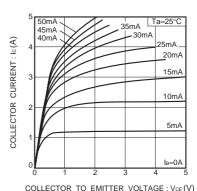


Fig.3 Grounded emitter output characteristics (II)

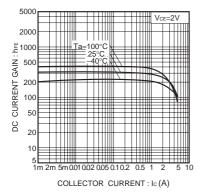


Fig.4 DC current gain vs. collector current

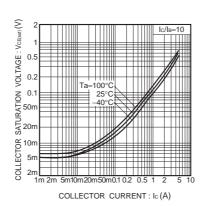


Fig.5 Collector-emitter saturation voltage vs. collector current (I)

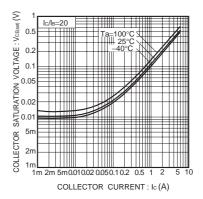


Fig.6 Collector-emitter saturation voltage vs. collector curren (II)

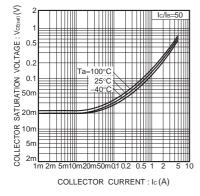


Fig.7 Collector-emitter saturation voltage vs. collector current (III)

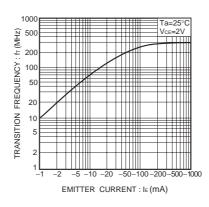


Fig.8 Gain bandwidth product vs. emitter current

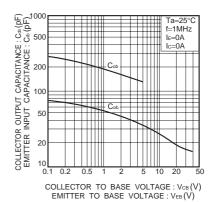


Fig.9 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

Notes

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