

Current Transducer LAX SERIES

Ref: LAX 100-NP

For the electronic measurement of currents: DC, AC, pulsed, with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).



Features

- Closed loop (compensated) current transducer using the hall effect
- Printed circuit board mounting
- Isolated plastic case recognized according to UL 94-V0
- Multirange with a single device: 16, 25, 33, 50 and 100 A rms
- 3 independent primary jumpers.

Advantages

- Excellent accuracy
- Very good linearity
- Low temperature drift
- Optimized response time
- Wide frequency bandwidth
- No insertion losses
- High immunity to external interference
- Current overload capability
- Height less than 16.5 mm for a simplified integration with power modules
- Low primary inductance.

Applications

- AC variable speed drives and servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS)
- · Power supplies for welding applications.

Standards

- EN 50178
- UL508 UR marking
- IEC 61010-1-safety.

Application Domain

Industrial.

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Electrical data

At $T_A = 25^{\circ}$ C, $V_C = \pm 15$ V and $R_M = 50 \Omega$, $N_P = 1$ turn, high speed PCB design (see page 11), unless otherwise noted. Parameters with a * in the conditions column apply over the - 40°C. 85°C ambient temperature range.

Parameter	Symbol	Unit	Mini	Тур	Maxi	Conditions	
Primary nominal current rms	I _{PN}	At			100		Apply derating according to fig. 2 and 3
Primary current, measuring range	I _{PM}	At	± 160			*	$V_{\rm C} = \pm 15 \text{V} \pm 5\%$
			49		51	*	$V_{C} = \pm 15 \text{ V} \pm 5\%, \pm I_{PM}$ measuring range, DC primary current
Measuring resistance	\mathbf{R}_{M}	Ω	0		51	*	$V_{C} = \pm 15 V \pm 5\%$, $\pm I_{PM}$ measuring range, AC primary current
			0		15	*	$V_{C} = \pm 12 V \pm 5\%$, $\pm I_{PM}$ measuring range, DC or AC primary current
Secondary nominal current rms	I _{SN}	mA		50			at 100 At
Supply voltage	٧c	V	± 11.4		± 15.75		
Current consumption	Ι _C	mA		10 + I _S	12 + I _S		
Electrical offset current	I _{OE}	μA	-150	0	+150		V _C = full range
				31	65		after a cycle to 50 A
Magnetic offset current		μΑ		41	70		after a cycle to 100 A
Magnetic onset current	l _{ом}			100	230		after a cycle to 300 A
				120	250		after a cycle to 500 A
		μΑ	-200		200		0° C + 70°C, V _C = full range
Temperature variation of \mathbf{I}_{O}	I _{OT}		-330		330		- 25°C + 85°C, V_c = full range
			-500		500	*	- 40°C + 85°C, V _C = full range
Sensitivity	G	mA/At		0.5			
Primary turns	N _P		1		3		
Sensitivity error	€G	%	-0.47		0.47		± 100 A range
		Ø		0.1	0.28		± 50 A range
Linearity error	e	% of range		0.06	0.14		± 100 A range
				0.11	0.32		± 100 A range, low speed 1 turn PCB design
Overall accuracy	X _G	%	-0.91		0.91		$= I_{OE} + \mathbf{e}_{G} + \mathbf{e}_{L}$
Reaction time	t _{ra}	μs		0.1			high and low speed PCB designs, dl _P /dt = 100 A/µs
di/dt accurately followed	di/dt	A/µs		> 100			
				0.17			0.1 Hz < f < 49 Hz, I _P = 0
Output current noise	I _{no}	μA_{rms}		0.17			51 Hz < f < 1 kHz, I _P = 0
				0.11			1 kHz < f < 100 kHz, I _P = 0
Secondary coil resistance	R _S	Ω		60			



Electrical data (continued)

	Symbol	Unit	N _P	High speed PCB design	Low speed PCB design	Conditions	
			1 turn	0.1	12		
Response time to 90 % of I_{PN} step (typical)	tr	μs	2 turns	0.1	0.2	$dI_P/dt = 100 \text{ A/}\mu\text{s}$	
			3 turns	0.1	0.2		
Frequency bandwidth (typical)	BW	kHz	1 turo	> 300	12.2	I _P = 25 A, -1 dB	
			1 turn	> 300	146	I _P = 25 A, -3 dB	
Frequency bandwidth (typical)	BW	kHz	2 turns	> 300	60	I _P = 12.5 A, -1 dB	
			2 turns	> 300	> 300	$I_P = 12.5 \text{ A}, -3 \text{ dB}$	
Fraguency bandwidth (typical)	BW	kHz	2 turno	> 300	50	I _P = 8.3 A, -1 dB	
Frequency bandwidth (typical)	BW		3 turns	> 300	> 300	I _P = 8.3 A, -3 dB	

Absolute maximum ratings

070615/8

	Symbol	Unit	Conditions
Primary AC current rms (3 primary jumpers in parallel)	I _{PN}	А	100 A up to $T_A = 70^{\circ}C$. Linear derating to 70 A at 85°C. See figure 2
Primary AC current rms (3 primary jumpers in parallel)	I _{PN}	A	at 85°C, high or low speed design 70 A up to 2 kHz See figure 3
Primary continuous direct current (3 primary jumpers in parallel)	I _{PN DC}	A	100 A up to $T_A = 50$ °C. Linear derating to 50 A at 85 °C. See figure 2
Primary DC or rms current (each jumper)		A	above value divided by 3
Maximum supply voltage (not operating)	v _c	V	± 20
Minimum measuring resistance	R _{M mini}	Ω	See measuring resistance in "Electrical data" table
Maximum busbar temperature (jumper)		°C	100
Ambient operating temperature	T _A	°C	- 40 + 85
Ambient storage temperature	Ts	°C	- 40 + 90

Stresses above these ratings may cause permanent damage. Exposure to absolute maximum ratings for extended periods may degrade reliability.



Isolation characteristics

	Symbol	Unit	Value
Rms voltage for AC isolation test, 50 Hz, 1 min, between primary and secondary	V _d	kV	3.5
Impulse withstand voltage 1.2/50 µs	$\hat{\mathbf{V}}_{\mathbf{w}}$	kV	8
Partial discharge extinction voltage rms @ 10pC	V _e	kV	> 1.3
Creepage distance	dCp	mm	8
Clearance distance	dCl	mm	8
Comparative Tracking Index	СТІ	V	600

Isolation application example

The transducer can be used according to EN 50178 and IEC 61010-1 standards under following conditions (for example):

- Rated isolation voltage: 600 V
- Reinforced isolation
- Over voltage category OV III
- Pollution degree PD2
- Non-uniform field

The creepage distance and clearance of the transducer mounted on a PCB are greater than 8 mm only if the primary circuit tracks stay out of the shaded area shown below:









LAX series 16-100 A

Typical performance characteristics



Figure 2: Current derating



Figure 4: Minimum measuring resistance (DC)



Figure 6: Linearity error



Figure 3: Frequency derating



Figure 5: Maximum measuring resistance



Figure7: Electrical offset drift

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Typical performance characteristics (continued)



Primary current (A)











(low speed 1 turn PCB design) $(I_{p} = 1.6 A)$

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LAX series 16-100 A

Typical performance characteristics (continued)



Figure 14: consumption current



Figure 16: Typical di/dt follow-up (low speed 1 turn PCB design)



Figure 15: Typical di/dt follow-up (high speed 1 turn PCB design)



Figure 17: Typical di/dt follow-up (low speed 1 turn PCB design)

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Performance parameters definition

The schematic used to measure all electrical parameters is: ($C_1 = C_2 = 100 \text{ nF}, R_M = 50 \Omega$ unless otherwise noted):



Figure 18: standard characterization schematics

Ampere-turns and amperes

The LAX transducer is sensitive to the primary current linkage $\Theta_{_{\rm P}}$ (also called ampere-turns).

$$\Theta_{P} = N_{P}I_{P}$$
 (At)

With N_p the number of primary turn (1, 2 or 3 depending on the connection of the primary jumpers)

Warning : As most LAX user will use it with only one single primary turn ($N_p = 1$), most of this datasheet is written with primary currents instead of current linkages. The unit is kept as ampere-turn (At) to make clear that ampere-turns are meant.

Transducer simplified model

The static model of the transducer at temperature T_A is:

$$I_s = G \Theta_P$$
 + error

In which error =
$$I_{OE} + I_{OT}(T_A) + \mathbf{e}_G \Theta_P + \mathbf{e}_L(\Theta_{Pmaxi})\Theta_{Pmaxi}$$

With: $\Theta_{P} = N_{P}I_{P}$: the input ampere-turns (At) Please read above warning. $\Theta_{\rm Pmaxi}$: the maxi input ampere-turns that have been applied to the transducer (At) : the secondary current (A) : the ambient temperature (°C) : the electrical offset current (A) OE $I_{OT}(T_{A})$: the temperature variation of I at temperature T_A (A) G : the sensitivity of the transducer (A/At) : the sensitivity error e $\mathbf{e}_{\mathrm{L}}(\Theta_{\mathrm{Pmaxi}})$: the linearity error for Θ_{Pmaxi}

This model is valid for primary ampere-turns $\Theta_{\rm p}$ between - $\Theta_{\rm Pmaxi}$ and + $\Theta_{\rm Pmaxi}$ only.

At zero input current, the model for the offset is reduced to:

$$I_{\rm S} = I_{\rm OE} + I_{\rm OT}({\rm T_A}) + I_{\rm OM}(\Theta_{\rm Pmaxi})$$

In which $\mathbf{I}_{_{OM}}(\Theta_{_{Pmaxi}})$ is the magnetic offset current due to the maximum input ampere-turns that have been applied to the transducer.

Sensitivity and linearity

To measure sensitivity and linearity, the primary current (DC) is cycled from 0 to $\rm I_p,$ then to $\rm -I_p$ and back to 0 (equally spaced $\rm I_p/$ 10 steps).

The sensitivity G is defined as the slope of the linear regression line for a cycle between \pm $I_{_{\rm PN}}\!.$

The linearity error $\mathbf{e}_{\rm L}$ is the maximum positive or negative difference between the measured points and the linear regression line, expressed in % of the maximum measured value.

Magnetic offset

The magnetic offset current ${\rm I}_{\rm OM}$ is the consequence of a current on the primary side ("memory effect" of the transducer's ferromagnetic parts). It is included in the linearity figure but can be measured individually.

It is measured using the following primary current cycle. $I_{\rm OM}$ depends on the current value $I_{\rm P1}.$

$$I_{OM} = \frac{Is(t_1) - Is(t_2)}{2}$$



Figure 19: current cycle used to measure magnetic and electrical offset (transducer supplied)

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LAX series 16-100 A

Performance parameters definition (continued)

Electrical offset

The electrical offset current I_{OE} can either be measured when the ferro-magnetic parts of the transducer are:

- · completely demagnetized, which is difficult to realize, or
- in a known magnetization state, like in the current cycle shown above.

Using the current cycle shown in figure 19, the electrical offset is:

$$I_{OE} = \frac{Is(t_1) + Is(t_2)}{2}$$

The temperature variation $I_{_{OT}}$ of the electrical offset current $I_{_{OE}}$ is the variation of the electrical offset from 25°C to the considered temperature:

$$I_{OT}(T) = I_{OE}(T) - I_{OE}(25^{\circ}C)$$

Note: the transducer has to be demagnetized prior to the application of the current cycle (for example with a demagnetization tunnel).

Overall accuracy

The overall accuracy at 25°C X_G is the error in the - I_{PN} ... + I_{PN} range, relative to the rated value I_{PN} . It includes:

- the electrical offset I
- the sensitivity error \mathbf{e}_{G}
- the linearity error \mathbf{e}_{1} (to \mathbf{I}_{PN})

The magnetic offset is part of the overall accuracy. It is taken into account in the linearity error figure provided the transducer has not been magnetized by a current higher than I_{PN} .

Response and reaction times

The response time $t_{\!_{\!\!\!\!\!\!}}$ and the reaction time $t_{\!_{\!\!\!\!\!\!\!\!\!\!\!\!\!\!}}$ are shown in the next figure.

Both depend on the primary current di/dt. They are measured at nominal ampere-turns.

The "di/dt accurately followed" mentionned in the electrical data table is defined as the di/dt of the primary current for which the response time is equal to 1 μ s.



Figure 20: response time t_r and reaction time t_r

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Application data

The LAX 100-NP has been designed to be used at nominal currents from 16 to 100 A. The 3 primary jumpers allow the adaptation of the number of primary turns N_p to the application so as to achieve the best compromise between nominal current, measuring range and secondary current:

Primary nominal current rms I _{PN} (A)	Number of primary turns N _P	Secondary nominal current rms I _{SN} (mA)	Primary current, measuring range I _{PM} (A)	Primary coil resistance @ 20°C R _P (μ W)	Primary insertion inductance L _P (nH)	Connections (see PCB layout)	
100	1	50	160	90	15	0-0-0 IN 0-0-0 OUT	
50	1	25	160	90	15	0-0-0 IN 0-0-0 OUT	
33.3	2	33.3	80	400	60	OOO IN OOO OUT	
33.3	3	50	53	800	136	OOO IN OOO OUT	
25	2	25	75	400	60	000 IN 00-0 OUT	
16.7	3	25	50	800	136	OOO IN OOO OUT	

See also the paragraph "performance parameters definition: transducer simplified model" for more details about ampereturns and output current.

High and low speed PCB designs

The PCB design is very important to achieve good linearity and frequency response with the LAX 100-NP. High speed designs are best for accuracy, high frequency response and low response times.





High and low speed PCB designs





Dimensions LAX 100-NP (in mm. General linear tolerance ± 0.15 mm)



Assembly on PCB

- Recommended PCB hole diameter 2 mm (0/+0.1) for primary pins
- Maximum PCB thickness
- Solder temperature
- 2.4 mm maximum 270°C for 15 s (wave

1 mm (0/+0.1) for secondary pins

soldering)

No-clean process only

Remarks

- I_{s} is positive (sourcing) when I_{p} flows in the direction of the arrow
- Mass: 20 g

Safety



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the following manufacturer's operating instructions.



Caution, risk of electrical shock

When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply).

Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a built-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used. Main supply must be able to be disconnected.