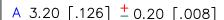
# HZ1206E152R-10

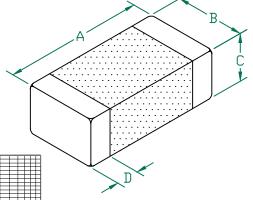


PHYSICAL DIMENSIONS:

B 1.60 [.063] ± 0.20 [.008]

C 1.10 [.043] ± 0.20 [.008]

D 0.51 [.020] ± 0.25 [.010]



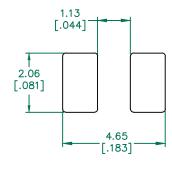
ELECTRICAL CHARACTERISTICS:											
Z @ 50MH ( Ω )	Ηz	DCR $\left(\begin{array}{c}\Omega\end{array} ight)$	Rated Current								
Nominal 15											
Minimum	1250										
Maximum	1875	0.300	500 mA								

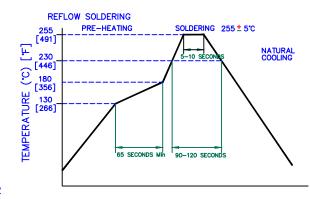
## NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 3000 PCS/REEL.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. TERMINATION FINISH IS 100% TIN.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)

### LAND PATTERNS FOR REFLOW SOLDERING

### RECOMMENDED SOLDERING CONDITIONS





(For wave soldering, add 0.762 [0.030] to this dimension)

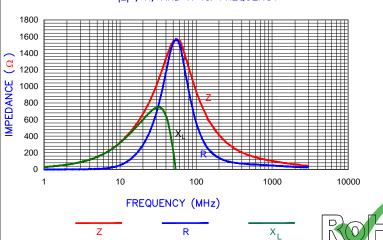
	[0.030] to this dimension)							
				This print is the property of Laird Tech, and is loaned in confidence				
				subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are		Laird		
						a	IJŲ	
С	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	reserved. PROJECT/PART NUMBER:		PART TYP		DRAWN BY
В	UPDATE COMPANY LOGO AND ROHS SYMBOL	03/24/08	JRK	HZ1206E152R-10	٥	C CO-F		JRI
Α	ORIGINAL DRAFT	02/16/05	JRK	02/16/05		TS	SHEET:	
REV	DESCRIPTION	DATE	INT	HZ1206E152R-10-C	DL#	- 🗍	1	of 1

#### 

Z vs FREQUENCY

IMPEDANCE UNDER DC BIAS

|Z| , R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture REF. 4405