# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

#### **General Description**

The MAX4200–MAX4205 are ultra-high-speed, openloop buffers featuring high slew rate, high output current, low noise, and excellent capacitive-load-driving capability. The MAX4200/MAX4201/MAX4202 are single buffers, while the MAX4203/MAX4204/MAX4205 are dual buffers. The MAX4201/MAX4204 have integrated 50 $\Omega$  termination resistors, making them ideal for driving 50 $\Omega$  transmission lines. The MAX4202/MAX4205 include 75 $\Omega$  backtermination resistors for driving 75 $\Omega$  transmission lines. The MAX4200/MAX4203 have no internal termination resistors.

The MAX4200–MAX4205 use a proprietary architecture to achieve up to 780MHz -3dB bandwidth, 280MHz 0.1dB gain flatness, 4200V/µs slew rate, and ±90mA output current drive capability. They operate from ±5V supplies and draw only 2.2mA of quiescent current. These features, along with low-noise performance, make these buffers suitable for driving high-speed analog-to-digital converter (ADC) inputs or for data-communications applications.

#### Features

- 2.2mA Supply Current
- High Speed
  - 780MHz -3dB Bandwidth (MAX4201/MAX4202)
  - 280MHz 0.1dB Gain Flatness (MAX4201/MAX4202)
  - 4200V/µs Slew Rate
- Low 2.1nV/√Hz Voltage-Noise Density
- Low 0.8pA/<del>/Hz</del> Current-Noise Density
- High ±90mA Output Drive (MAX4200/MAX4203)
- Excellent Capacitive-Load-Driving Capability
- Available in Space-Saving SOT23 or µMAX<sup>®</sup> Packages

#### Applications

- High-Speed DAC Buffers
- Wireless LANs
- Digital-Transmission Line Drivers
- High-Speed ADC Input Buffers
- IF/Communications Systems



## **Typical Application Circuit**

µMAX is a registered trademark of Maxim Integrated Products, Inc.



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## **Absolute Maximum Ratings**

Supply Voltage (V <sub>CC</sub> to V <sub>FF</sub> )	+12V
Voltage on Any Pin to GND (V <sub>EE</sub> - 0.3V) to (V	( <sub>CC</sub> + 0.3V)
Output Short-Circuit Duration to GND	Continuous
Continuous Power Dissipation (T <sub>A</sub> = +70°C)	
	E74 \\\

5-Pin SOT23 (derate 7.1mW/°C above +70°C) .......571mW 8-Pin μMAX (derate 4.1mW/°C above +70°C) ......330mW 8-Pin SO (derate 5.9mW/°C above +70°C) ......471mW

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+260°C
Junction Temperature	+150°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **DC Electrical Characteristics**

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, R<sub>L</sub> =  $\infty$ , T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C.)

PARAMETER	SYMBOL		CONDITIONS			TYP	MAX	UNITS	
Operating Supply Voltage	VS	Guarantee	Guaranteed by PSR test				±5.5	V	
Quiescent Supply Current	۱ <sub>S</sub>	Per buffer,	V <sub>IN</sub> = 0V			2.2	4	mA	
Input Offset Voltage	V <sub>OS</sub>	V <sub>IN</sub> = 0V				1	15	mV	
Input Offset Voltage Drift	TCV <sub>OS</sub>	V <sub>IN</sub> = 0V				20		μV/°C	
Input Offset Voltage Matching		MAX4203/I	MAX4204/MAX42	205		0.4		mV	
Input Bias Current	Ι <sub>Β</sub>					0.8	10	μA	
Input Resistance	R <sub>IN</sub>	(Note 1)				500		kΩ	
		-3.0V ≤	MAX4200/MAX	4203, R <sub>EXT</sub> = 150Ω	0.9	0.96	1.1		
Voltage Gain	oltage Gain A <sub>V</sub> N	V <sub>OUT</sub> ≤	MAX4201/MAX	4204, R <sub>EXT</sub> = 50Ω	0.42	0.50	0.58	V/V	
		3.0V	3.0V MAX4202/MAX4205, R <sub>EXT</sub> = 75Ω			0.50	0.59		
Power-Supply Rejection	PSR	$V_{\rm S}$ = ±4V t	o ±5.5V		55	72		dB	
			MAX4			8		Ω	
Output Resistance	R <sub>OUT</sub>	f = DC MAX4201/MAX4204 MAX4202/MAX4205		MAX4201/MAX4204		50			
					75				
		MAX4200/MAX4203   RL = 30Ω MAX4201/MAX4204   MAX4202/MAX4205 MAX4202/MAX4205		MAX4200/MAX4203		±90			
Output Current	IOUT			MAX4201/MAX4204		±52		mA	
					±44				
				MAX4200/MAX4203		150			
Short-Circuit Output Current	I <sub>SC</sub>			MAX4201/MAX4204		90		mA	
			MAX4202/MAX42			75			
			R <sub>L</sub> = 150Ω		±3.3	±3.8			
Output-Voltage Swing		MAX4200/	MAX4203	R <sub>L</sub> = 100Ω	±3.2	±3.7		v	
	V <sub>OUT</sub>			R <sub>L</sub> = 37.5Ω		±3.3			
		MAX4201/	MAX4204	R <sub>L</sub> = 50Ω	±1.9	±2.1		1	
		MAX4202/MAX4205		R <sub>L</sub> = 75Ω	±2.0	±2.3			

# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

## **AC Electrical Characteristics (continued)**

 $(V_{CC} = +5V, V_{EE} = -5V, R_L = 100\Omega$  for MAX4200/MAX4201/MAX4203/MAX4204,  $R_L = 150\Omega$  for MAX4202/MAX4205,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}$ C.)

PARAMETER	SYMBOL			CONDI	IONS		MIN	TYP	MAX	UNITS
					MAX	1200		660		
3dB Bandwidth	BW <sub>(-3dB)</sub>	V <sub>OUT</sub> ≤ 100mV <sub>RMS</sub> -		MAX	MAX4201/MAX4202 MAX4203		780			
-3dB Bandwidth				MAX			530		MHz	
				MAX	4204/MAX4205		720			
		N/		MAX	1200		220			
	DW			MAX	1201/MAX4202		280			
0.1dB Bandwidth	BW <sub>(0.1dB)</sub>	V <sub>OUT</sub> ≤ 100	umv <sub>RI</sub>	MS	MAX	1203		130		MHz
					MAX	4204/MAX4205		230		
	50014			MAX42	00/MAX4	201/MAX4202		490		• • • •
Full-Power Bandwidth	FPBW	V <sub>OUT</sub> ≤ 2V	P-P	MAX42	03/MAX4	204/MAX4205		310		MHz
Slew Rate	SR	V <sub>OUT</sub> = 2V	step	1				4200		V/µs
Group Delay Time								405		ps
Settling Time to 0.1%	ts	V <sub>OUT</sub> = 2V	step					12		ns
	SFDR					f = 5MHz		-48		
Spurious-Free Dynamic		V <sub>OUT</sub> = 2V <sub>P-P</sub>			f = 20MHz		-45			
						f = 100MHz		-34		dBc
Range						f = 5MHz		-47		
					X4204/	f = 20MHz	1	-44		
			101/203	f = 100MHz	f = 100MHz		-32			
		MAX4200/MAX4201/ MAX4202, f = 500kHz,		201/	Second	l harmonic		-72		
						armonic		-48		
		V <sub>OUT</sub> = 2V	<sub>UT</sub> = 2V <sub>P-P</sub>		Total ha	armonic		-48		15
Harmonic Distortion	HD	MAX4203/	MAX4203/MAX4204/		Second	l harmonic		-83		dBc
		MAX4205, f = 50				armonic	-47			-
		V <sub>OUT</sub> = 2V	P-P		Total ha	armonic		-47		
Differential Gain Error	DG	NTSC, RL	= 1500	2				1.3		%
Differential Phase Error	DP	NTSC, RL :	= 1500	2				0.15		degrees
Input Voltage-Noise Density	e <sub>n</sub>	f = 1MHz				2.1		nV/√Hz		
Input Current-Noise Density	in	f = 1MHz				0.8		pA/√Hz		
Input Capacitance	C <sub>IN</sub>					2		pF		
Output Impedance	Z <sub>OUT</sub>	f = 10MHz						6		Ω
				f = 10N	IHz	-87 -65			<u> </u>	
Amplifier Crosstalk	X <sub>TALK</sub>			f = 100	MHz			dB		

**Note 1:** Tested with no load; increasing load will decrease input impedance.

## Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

### **Typical Operating Characteristics**

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, R<sub>L</sub> = 100 $\Omega$  for MAX4200/MAX4201/MAX4203/MAX4204, R<sub>L</sub> = 150 $\Omega$  for MAX4202/MAX4205, unless otherwise noted.)



## Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

### **Typical Operating Characteristics (continued)**

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, R<sub>L</sub> = 100 $\Omega$  for MAX4200/MAX4201/MAX4203/MAX4204, R<sub>L</sub> = 150 $\Omega$  for MAX4202/MAX4205, unless otherwise noted.)



# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

### **Typical Operating Characteristics (continued)**

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, R<sub>L</sub> = 100 $\Omega$  for MAX4200/MAX4201/MAX4203/MAX4204, R<sub>L</sub> = 150 $\Omega$  for MAX4202/MAX4205, unless otherwise noted.)



# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

## **Typical Operating Characteristics (continued)**

 $(V_{CC} = +5V, V_{EE} = -5V, R_L = 100\Omega$  for MAX4200/MAX4201/MAX4203/MAX4204,  $R_L = 150\Omega$  for MAX4202/MAX4205, unless otherwise noted.)



# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

## **Pin Configurations**



### **Pin Description**

	PIN			
MAX4200/MA)	(4201/MAX4202	MAX4203 MAX4204	NAME	FUNCTION
SOT23-5	SO	MAX4205 SO/μMAX		
50123-5	50			
1	1, 2, 5, 8	—	N.C.	No Connection. Not Internally Connected
3	3	—	IN	Buffer Input
—	_	1	IN1	Buffer 1 Input
_	_	2	OUT1	Buffer 1 Output
2	4	_	V <sub>EE</sub>	Negative Power Supply
_	_	3	V <sub>EE1</sub>	Negative Power Supply for Buffer 1
_	_	4	V <sub>EE2</sub>	Negative Power Supply for Buffer 2
—	—	5	IN2	Buffer 2 Input
_	_	6	OUT2	Buffer 2 Output
5	6	_	OUT	Buffer Output
4	7	—	V <sub>CC</sub>	Positive Power Supply
_	_	7	V <sub>CC2</sub>	Positive Power Supply for Buffer 2
—	—	8	V <sub>CC1</sub>	Positive Power Supply for Buffer 1

# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

### **Detailed Description**

The MAX4200–MAX4205 wide-band, open-loop buffers feature high slew rates, high output current, low  $2.1nV\sqrt{Hz}$  voltage-noise density, and excellent capacitive-load-driving capability. The MAX4200/MAX4203 are single/dual buffers with up to 660MHz bandwidth, 230MHz 0.1dB gain flatness, and a 4200V/µs slew rate. The MAX4201/MAX4204 single/dual buffers with integrated 50 $\Omega$  output termination resistors, up to 780MHz bandwidth, 280MHz gain flatness, and a 4200V/µs slew rate, are ideally suited for driving high-speed signals over 50 $\Omega$  cables. The MAX4202/MAX4205 provide bandwidths up to 720MHz, 230MHz gain flatness, 4200V/µs slew rate, and integrated 75 $\Omega$  output termination resistors for driving 75 $\Omega$  cables.

With an open-loop gain that is slightly less than +1V/V, these devices do not have to be compensated with the internal dominant pole (and its associated phase shift) that is present in voltage-feedback devices. This feature allows the MAX4200–MAX4205 to achieve a nearly constant group delay time of 405ps over their full frequency range, making them well suited for a variety of RF and IF signal-processing applications.

These buffers operate with  $\pm$ 5V supplies and consume only 2.2mA of quiescent supply current per buffer while providing up to  $\pm$ 90mA of output current drive capability.

### **Applications Information**

#### **Power Supplies**

The MAX4200–MAX4205 operate with dual supplies from  $\pm 4V$  to  $\pm 5.5V$ . Both V<sub>CC</sub> and V<sub>EE</sub> should be bypassed to the ground plane with a 0.1µF capacitor located as close to the device pin as possible.

#### **Layout Techniques**

Maxim recommends using microstrip and stripline techniques to obtain full bandwidth. To ensure that the PC board does not degrade the amplifier's performance, design it for a frequency greater than 6GHz. Pay careful attention to inputs and outputs to avoid large parasitic capacitance. Whether or not you use a constantimpedance board, observe the following guidelines when designing the board:

- Do not use wire-wrap boards, because they are too inductive.
- Do not use IC sockets, because they increase parasitic capacitance and inductance.
- Use surface-mount instead of through-hole components for better high-frequency performance.

- Use a PC board with at least two layers; it should be as free from voids as possible.
- Keep signal lines as short and as straight as possible. Do not make 90° turns; round all corners.

#### Input Impedance

The MAX4200–MAX4205 input impedance looks like a 500k $\Omega$  resistor in parallel with a 2pF capacitor. Since these devices operate without negative feedback, there is no loop gain to transform the input impedance upward, as in closed-loop buffers. As a consequence, the input impedance is directly related to the output impedance. If the output load impedance decreases, the input impedance also decreases. Inductive input sources (such as an unterminated cable) may react with the input capacitance and produce some peaking in the buffer's frequency response. This effect can usually be minimized by using a properly terminated transmission line at the buffer input, as shown in Figure 1.



Figure 1. Using a Properly Terminated Input Source

#### **Output Current and Gain Sensitivity**

The absence of negative feedback means that open-loop buffers have no loop gain to reduce their effective output impedance. As a result, open-loop devices usually suffer from decreasing gain as the output current is decreased. The MAX4200–MAX4205 include local feedback around the buffer's class-AB output stage to ensure low output impedance and reduce gain sensitivity to load variations. This feedback also produces demand-driven current bias to the output transistors for ±90mA (MAX4200/MAX4203) drive capability that is relatively independent of the output voltage (see *Typical Operating Characteristics*).

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#### **Output Capacitive Loading and Stability**

The MAX4200–MAX4205 provide maximum AC performance with no load capacitance. This is the case when the load is a properly terminated transmission line. However, these devices are designed to drive any load capacitance without oscillating, but with reduced AC performance.

Since the MAX4200–MAX4205 operate in an openloop configuration, there is no negative feedback to be transformed into positive feedback through phase shift introduced by a capacitive load. Therefore, these devices will not oscillate with capacitive loading, unlike similar buffers operating in a closed-loop configuration. However, a capacitive load reacting with the buffer's output impedance can still affect circuit performance. A capacitive load will form a lowpass filter with the buffer's output resistance, thereby limiting system bandwidth. With higher capacitive loads, bandwidth is dominated by the RC network formed by  $R_T$  and  $C_L$ ; the bandwidth of the buffer itself is much higher. Also note that the isolation resistor forms a divider that decreases the voltage delivered to the load.

Another concern when driving capacitive loads results from the amplifier's output impedance, which looks inductive at high frequency. This inductance forms an L-C resonant circuit with the capacitive load and causes peaking in the buffer's frequency response. Figure 2 shows the frequency response of the MAX4200/ MAX4203 under different capacitive loads. To settle out some of the peaking, the output requires an isolation resistor like the one shown in Figure 3. Figure 4 is a plot of the MAX4200/MAX4203 frequency response with capacitive loading and a 10 $\Omega$  isolation resistor. In many applications, the output termination resistors included in the MAX4201/MAX4202/ MAX4204/MAX4205 will serve this purpose, reducing component count and board space. Figure 5 shows the MAX4201/MAX4202/ MAX4204/MAX4205 frequency response with capacitive loads of 47pF, 68pF, and 120pF.

#### **Coaxial Cable Drivers**

Coaxial cable and other transmission lines are easily driven when properly terminated at both ends with their characteristic impedance. Driving back-terminated transmission lines essentially eliminates the line's capacitance. The MAX4201/MAX4204, with their integrated 50 $\Omega$  output termination resistors, are ideal for driving 50 $\Omega$  cables. The MAX4202/MAX4205 include integrated 75 $\Omega$  termination resistors for driving 75 $\Omega$  cables. Note that the output termination resistor forms a voltage divider with the load resistance, thereby decreasing the amplitude of the signal at the receiving end of the cable by one half (see the *Typical Application Circuit*).

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Figure 2. MAX4200/MAX4203 Small-Signal Gain vs. Frequency with Load Capacitance and No Isolation Resistor



Figure 3. Driving a Capacitive Load Through an Isolation Resistor



Figure 4. MAX4200/MAX4203 Small-Signal Gain vs. Frequency with Load Capacitance and  $10\Omega$  Isolation Resistor



Figure 5. MAX4201/MAX4202/MAX4204/MAX4205 Small-Signal Gain vs. Frequency with Capacitive Load and No External Isolation Resistor

# Ultra-High-Speed, Low-Noise, Low-Power, SOT23 Open-Loop Buffers

## **Ordering Information**

PART	PIN-PACKAGE	TOP MARK	PKG CODE
MAX4200ESA	8 SO	—	S8-2
MAX4200EUK-T	5 SOT23-5	AABZ	U5-1
MAX4201ESA	8 SO	—	S8-2
MAX4201EUK-T	5 SOT23-5	ABAA	U5-1
MAX4202ESA	8 SO	_	S8-2
MAX4202EUK-T	5 SOT23-5	ABAB	U5-1
MAX4203ESA	8 SO	_	S8-2
MAX4203EUA-T	8 µMAX-8	—	U8-1
MAX4204ESA	8 SO	_	S8-2
MAX4204EUA-T	8 µMAX-8	_	U8-1
MAX4205ESA	8 SO	_	S8-2
MAX4205EUA-T	8 µMAX-8	_	U8-1

**Note:** All devices are specified over the -40°C to +85°C operating temperature range.

### **Chip Information**

TRANSISTOR COUNTS: MAX4200/MAX4201/MAX4202: 33 MAX4203/MAX4204/MAX4205: 67 SUBSTRATE CONNECTED TO V<sub>EE</sub>

#### **Selector Guide**

PART	NO. OF BUFFERS	INTERNAL OUTPUT TERMINATION (Ω)	PIN-PACKAGE
MAX4200	1	—	8 SO, 5 SOT23
MAX4201	1	50	8 SO, 5 SOT23
MAX4202	1	70	8 SO, 5 SOT23
MAX4203	2	—	8 SO/µMAX
MAX4204	2	50	8 SO/µMAX
MAX4205	2	75	8 SO/µMAX

### **Package Information**

For the latest package outline information and land patterns (footprints), go to <u>www.maximintegrated.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8-SOIC	S8-2	<u>21-0041</u>	<u>90-0096</u>
5-SOT23	U5-1	<u>21-0052</u>	<u>90-0174</u>
8-µMAX	U8-1	21-0036	90-0092

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## **Revision History**

REVISION NUMBER	REVISION DATES	DESCRIPTION	PAGES CHANGED
4	12/17	Updated Absolute Maximum Ratings section	2

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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