

Vishay High Power Products

Input Rectifier Diode, 10 A



PRODUCT SUMMARY					
V _F at 10 A	< 1 V				
I _{FSM}	200 A				
V _{RRM}	800/1200 V				

DESCRIPTION/FEATURES

The 10ETS..S rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product series has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS								
APPLICATIONS	SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS							
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	A					

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN					
I _{F(AV)}	Sinusoidal waveform	10	А				
V _{RRM}		800/1200	V				
I _{FSM}		200	A				
V _F	10 A, T _J = 25 °C	1.1	V				
TJ		- 40 to 150	°C				

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA				
10ETS08S	800	900					
10ETS10S	1000	1100	0.5				
10ETS12S	1200	1300					

ABSOLUTE MAXIMUM RATII	NGS			
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$T_C = 105 \ ^{\circ}C$, 180° conduction half sine wave	10	
Maximum peak one cycle		10 ms sine pulse, rated V_{RRM} applied	170	A
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	200	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied 130		A ² s
Maximum I-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	145	A-S
Maximum I ² √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied	1450	A²√s

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST	TEST CONDITIONS		UNITS	
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C	1.1	V		
Forward slope resistance	r _t	T.I = 150 °C	20	mΩ		
Threshold voltage	V _{F(TO)}	1j=150 C	0.82	V		
Movimum roverse leekege ourrent	I _{RM}	$T_J = 25 \ ^{\circ}C$	V - Poted V	0.05	mA	
Maximum reverse leakage current		T _J = 150 °C	$V_{R} = Rated V_{RRM}$	0.50		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W		
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		62	0/11		
Soldering temperature	Τs		240	°C		
Approximate weight			2	g		
			0.07	oz.		
			10ETS08S			
Marking device		Case style D ² PAK (SMD-220)	10ETS10S			
			10ETS12S			

Note

⁽¹⁾ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W For recommended footprint and soldering techniques refer to application note #AN-994



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Fig. 1 - Current Rating Characteristics



Fig. 2 - Current Rating Characteristics





Fig. 4 - Forward Power Loss Characteristics



Fig. 5 - Maximum Non-Repetitive Surge Current



Fig. 6 - Maximum Non-Repetitive Surge Current

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Fig. 7 - Forward Voltage Drop Characteristics



Fig. 8 - Thermal Impedance ZthJC Characteristics



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ORDERING INFORMATION TABLE

Device code	10	E	т	S	12	S	TRL	-
	1	2	3	4	5	6	7	8
	1	Circ		ng (10 = iguratior liode				
	3	- Pac	kage TO-220					
	4		e of silic Standaı	on d recov	ery recti	ifier	Г	08 = 80
	5 6			le x 100 D ² PAK		-	rsion	10 = 10 12 = 12
	7	• TF	-	ube pe and r pe and ı	-		-	
	8	• No	one = St	tandard ad (Pb)-1	product		,	

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95046					
Part marking information	http://www.vishay.com/doc?95054				
Packaging information	http://www.vishay.com/doc?95032				



Vishay

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