

Description

SPF0004 includes two N-channel power MOSFETs with zener diode for ESD protection. The package of SPF0004 isolates each MOSFET, and has heatsink connected to each drain.

Features

- Automotive Qualified
- Low On Resistance
- ESD Protection Zener on Gate
- 100% Avalanche Tested
- Compliant with RoHS directive
- V_{DSS} ------ 275 V ($I_D = 100 \,\mu A$) I_D ------ $\pm 6 \,A$

Applications

- DC/DC Converter
- Other Switched-mode Power Supply

Package





Not to scale

Internal Schematic Diagram



Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25 \ ^{\circ}C$

Parameter	Symbol	Test conditions	Rating	Unit
Drain to Source Voltage	V _{DSS}		275	V
Gate to Source Voltage	V _{GSS}		± 20	V
Continuous Drain Current	I _D		± 6	А
Pulsed Drain Current	I _{D(PULSE)}	Pulse width $\leq 100 \mu s$ Duty cycle $\leq 1 \%$	± 30	А
Single Pulse Avalanche Energy	E _{AS}	$V_{DD} = 49 \text{ V}, \\ L = 0.05 \text{ mH}, \\ I_{AS} = 40\text{ A}, \\ V_{GS} = +16 \text{ V}, -13 \text{ V}, \\ R_{G} = 1.5 \text{ k}\Omega, \\ \text{unclamped}, \\ \text{see Figure 1}$	47.5	mJ
Avalanche Current	I _{AS}		30	А
Power Dissipation	P _D	$T_C = 25 \ ^{\circ}C$	2.5	W
Drain to Source dv/dt 1	dv/dt 1	$V_{DD} = 200 \text{ V},$ L = 0.035 mH, R_G = 150 Ω , I_{DP} = 30 A, V_{GS} = +16 V, -16 V, di/dt $\ge -125 \text{ A/}\mu\text{s},$ see Figure 2	5.6	V/ns
Peak Diode Recovery dv/dt 2	dv/dt 2	$V_{DD} = 200 \text{ V},$ L = 0.2 mH, $I_{SDP} = 30 \text{ A},$ See Figure 3	8.5	V/ns
Peak Diode Recovery di/dt	di/dt	$V_{DD} = 200 \text{ V},$ L = 0.2 mH, $I_{SDP} = 30 \text{ A},$ See Figure 3	220	A/µs
Operating Junction Temperature	T _J		150	°C
Storage Temperature Range	T _{STG}		– 55 to 150	°C

Thermal Characteristics

Unless otherwise specified, $T_A = 25 \ ^{\circ}C$

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Thermal Resistance (Junction to Case)	$R_{ heta JC}$		_	_	4.7	°C/W

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Electrical Characteristics

Unless otherwise specified, $T_A = 25 \ ^{\circ}C$

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 100 \ \mu A, \ V_{GS} = 0 \ V$	275	_	-	V
Drain to Source Leakage Current	I _{DSS}	$V_{DS} = 275 V, V_{GS} = 0 V$			100	μΑ
Gate to Source Leakage Current	I _{GSS}	$V_{GS} \!=\! \pm 20 \ V$	-	—	10	μA
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.90	2.25	2.60	V
Forward Transconductance	R _{e(yfs)}	$V_{DS} = 10 \text{ V}, I_D = 6 \text{ A}$	_	20	-	S
Static Drain to Source On-Resistance	R _{DS(ON)}	$I_D = 6 A, V_{GS} = 10 V$	Ι	0.20	0.26	Ω
Input Capacitance	C _{iss}	$V_{DS} = 10 V$	_	960	05	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	-	250	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1 MHz	-	36	_	1
Turn-On Delay Time	t _{d(on)}	N 200 N	-4	15	-	
Rise Time	t _r	$V_{DD} = 200 V$ $I_{D} = 6 A$ $V_{GS} = 10 V, R_{G} = 10 \Omega$ Refer to Figure 4	A A	34	-	ns
Turn-Off Delay Time	t _{d(off)}		0-	112	_	
Fall Time	t _f		_	144	-	
Source to Drain Diode Forward Voltage	V _{SD}	$I_{SD} = 6 A, V_{GS} = 0 V$		-	1.2	V
Source to Drain Diode Reverse Recovery Time	t _{rr}	$I_{SDP} = 6 A$ di/dt = 100 A/µs Refer to Figure 3	-	117	_	ns

rr di/dt Refei

Test Circuits and Waveforms



Figure 3 Diode Reverse Recovery Time







Figure 8 R_{DS(ON)} vs. I_D characteristics (typ.)



Figure 11 V_{DSS} vs. T_C characteristics (typ.)

Figure 12 V_{th} vs. T_C characteristics (typ.)



Figure 15 Dynamic input / output characteristics (typ.)

Figure 16 Safe operating area



Figure 18. Transient Thermal Resistance

Physical Dimensions

• HSOP20 package



NOTES:

- Dimensions in millimeters
- Lead treatment: Pb-free (RoHS compliant)
- When soldering the products, make sure to minimize the working time, within the following limits: Reflow (MSL 3)

Preheat: 170 to 190 °C / 110 s Solder heating: 220 to 250 °C / 60s (3 times) Soldering iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time

Marking Diagram



X is control number (A to Z)

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