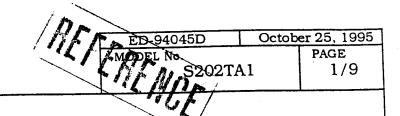
PREPARED BY: DATE:	<u> </u>	SPEC No.	ED-94045D
S. Suiwara Oct. 27. 95	SHARP	ISSUE	October 25, 1995
APPROVED BY: DATE:		PAGE	10 Pages
14. Imanaka Gct 27. 1995	ELECTRONIC COMPONENTS CROUP SHARP CORPORATION	REPRESEN	TATIVE DIVISION
	SPECIFICATION	OF DE	io-electronic vices div.
DEVI	CE SPECIFICATION FOR		`
	SOLID STATE RELAY		
100	71 N		-
MODE	S202TA1		
or cause anyone r 2. Please obey the in SHARP takes no r (1) This device is Main uses of Computer · Measuring (2) Please take pris used for th · Unit concert · Traffic signt · Other safet (3) Please do not · Space equit · Nuclear contact a SHARF	with reasonable care as important informatic eproduce them without Sharp's consent. Istructions mentioned below for actual use of esponsibility for damage caused by improper designed for general electronic equipment. This device are as follows: OA equipment Tooling machine AV equipment steps in order to maintain reliability are uses mentioned below which require high raining control and safety of a vehicle (air plantal Gas leak detection breaker Tire both y equipment, etc. Tuse for the uses mentioned below which recomment Telecommunication equipment (Trantrol equipment Medical equipment tetc. Prepresentative of sales office in advance whe pplications other than those applications for HARP at (1).	f this device. Tuse of the device quipment (Terminoment • Home and safety, in case reliability. e, train, automob x and burglar ala quire extremely h runk) en you intend to	es. nal) appliance, etc. this device pile etc.) arm box igh reliability.
CUSTOMER'S APPRO	DATE VAL PRESE BY	NTED C	T.M
DATE	De Er	Matsumura, epartment Gene igineering Dept oto-Electronic I	

ELECOM Group

SHARP CORPORATION

BY



1. Application

This specification applies to the outline and characteristics of SIP type Solid State Relay (SSR), Model No. S202TA1.

2. Outline

- 2.1 Refer to the attached drawing No. S3D93145C.
- 2.2 (1) Trade mark (2) Model No. and (3) Lot symbol shall be indicated on the surface.
- 3. Ratings and characteristics: Refer to the attached sheet, Page 3 to 5.
 - 3.1 Absolute maximum ratings
 - 3.2 Electrical characteristics
- 4. Reliability

Refer to the attached sheet, Page 6, 7.

5. Incoming inspection

Refer to the attached sheet, Page 7.

- 6. Supplements
 - 6.1 This product is not designed against irradiation.

This product is assembled with electrical input and output.

This product incorporates non-coherent light emitting diode.

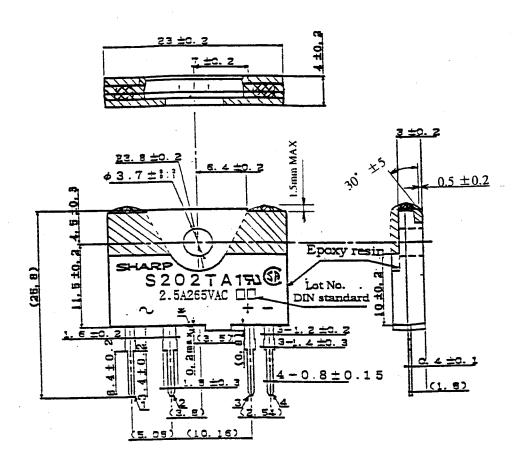
6.2 This Model is approved by UL and CSA.

UL file No.: E94758

CSA file No.: LR63705

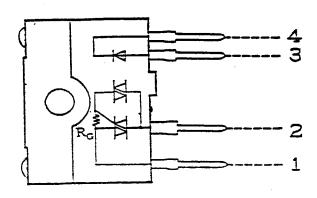
7. Notes

Refer to the attached sheet, Page 8, 9.



Pin finish: Solder plating

Pin Nos. and internal connection diagram



1) * mark does not allow external wiring.

2) (): TYP.

: Coating area (Epoxy resin)

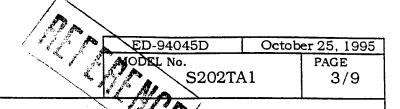
: There are cases where epoxy resin is put

in this area.

 $R_G = 100 \Omega \pm 10\%$

Pin No.	Connect	
1	Output (Triac T1)	
2	Output (Triac T2)	
3	Input (+)	
4	Input (-)	

SCALE	UNIT
2/1	1=1/1mm
Name	S202TA1 Outline Dimensions
Drawing No.	S3D93145C



3.1 Absolute maximum ratings

(Ta=25°C unless otherwise noted)

	Parameter	Symbol	Rating	Unit	Conditions
So	ldering temperature	Tsol	260	Ĵ	For 10 s
Oŗ	perating temperature	Topr	0 to +100	°	
St	orage temperature	Tstg	-40 to +125	Ç	
Oı	perating frequency	f	47 to 63	Hz	Ta=0℃ to 100℃
O _l	perating voltage range	Vout	25 to 265	Vrms	Ta=0℃ to 100℃
Ise	olation voltage (*1)	Viso	3000	Vrms	AC 60Hz, For 1min. RH=40 to 60%
Lo	oad power factor (Main triac)	PF	0.2 to 1.0		
	Forward current	I _F	50	mA	
Input	Peak forward current	I _{FM}	1	A(pk)	PW≦100 μs Duty Ratio 0.001
niput	Reverse voltage	V _R	6	V	
	Maximum junction temperature	Tj	125	Č	
	DMCtt-		2.5	Arms	Tc=85°C, With heat sink
	RMS on-state current	I_{T}	1.6	Arms	Tc=100℃, With heat sink
	Repetitive peak on-state current	I _{TRM}	20	A	50ms, Repeatable every 1 s, Tj<125°C
Output (Main	Peak one cycle surge current	Isurge	140	A	60Hz sine wave Tc=25℃ start
triac)	Repetitive peak off- state voltage	$V_{ m DRM}$	600	V	
	Critical rate of rise of on-state current	dI/dt	50	A/ μs	
	Main terminal fusing current	I ² t	80	A ² sec	t=8.3ms
	Junction temperature	Tj	125	°C	

(*1) Isolation voltage measuring method

- (1) Dielectric withstand tester with zero-cross circuit shall be used.
- (2) The wave form of applied voltage shall be sine wave.
- (3) It shall be applied voltage between input and output. (Inputs and outputs shall be short-circuited respectively)

ED-94045D	October 25, 1995
MODEL No.	PAGE
S202TA	A1 4/9

3.2 Electrical characteristics

(Ta=25°C Unless otherwise noted)

	Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Conditions
T	Forward voltage	$V_{\mathbf{F}}$	-	1.2	1.4	. V	I _F =20mA
Input	Reverse current	I _R	_	-	10-4	Α	V _R =3.0V
	Repetitive peak off-state current	I _{DRM}	-	<u>-</u> -	10 ⁻⁴ 10 ⁻³	A A	$Ta=25$ °C $I_F=0$ $Ta=100$ °C $V_D=V_{DRM}$
	On-state voltage (Main Triac)	V _{T1}	•	1.0	1.1	Vrms	I _F =8mA, R load I _T =2.5Arms
	On-state voltage (Phototriac)	V_{T2}	-	-	2.5	Vrms	I _F =8mA, R load I _T =14mA
Output	On-state operating current range (Main Triac)	I _{TM1}	25	-	2500	mArms	After latched R Load
Juiput	On-state operating current range (Phototriac)	I _{TM2}	10	-	25	mArms	Snubber circuit (Output): C=0.33 μ F R=820 Ω , R Load
	Holding current (Main Triac)	I _H	15	-	25	mA	
	Critical rate of rise of off-state voltage (Static)	(dv/dt)s	50	300	-	V/μs V/μs	Tj=25℃ Tj=125℃ V _D =400V
	Commutation critical rate of rise of off- state voltage	(dv/dt)c	5	-	-	V/μs	Tj=125°C, V _D =400V dIt/dt=-8.0A/ms
	Minimum trigger current	I _{FT}	-	-	8	mA	R Load I _{IM} MAX.
Transfer charac-	Isolation resistance	Riso	10 ¹⁰	-	-	Ω	DC500V RH=40 to 60%
teris-	Input-output capacitance	Ciso	-	1.2	-	pF	V=0, f=1.0MHz
	Turn on time	t _{ON}	-	-	1	ms	AC50Hz, I _F =8mA
	Turn off time	t _{OFF}	-	-	10	ms	AC50Hz, I _F =8mA
	Thermal resistance	Rth(j-c)	-	4.0	4.5	°C/W	Triac junction to case
	Thermal resistance	Rth(j-a)	-	40	45	°C/W	Triac junction to ambient
	Snubber capacitor	-	-	0.015	-	μF	
	Snubber resistor	-	-	27	-	Ω	

RMS on-state current derating curve

Fig. 1 Tc-I_T (rms) rating

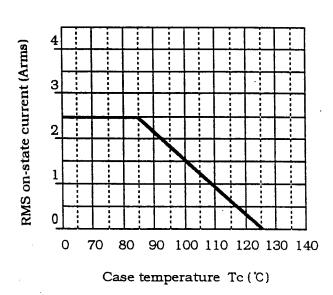
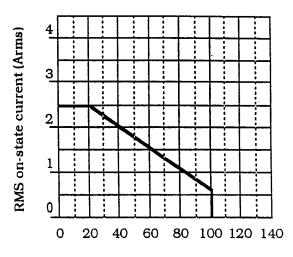


Fig. 2 Ta-I_T (rms) rating



Ambient temperature Ta(℃)

ED-94045D	October 25, 1995
MODEL No.	PAGE
S202TA	M 6/9
1	i '

4. Reliability

The reliability of products shall be satisfied with items listed below.

Confidence level: 90% LTPD: 10%/20%

Test Items	Test Conditions	Judgement Criteria	Samples (n) Defective (C)
Temperature cycling	1 cycle -40°C to 125°C (30min) (30min) 20 cycles test	(1) Forward voltage:	n=22, C=0
High temp. high humidity storage	+60℃, 90%RH, 500h	I±5% or less (2) Reverse current:	n=22, C=0
High temp. storage	+125°C, 1000h	USL×2 or less	n=22, C=0
Low temp. storage	-40°C, 1000h	(3) Repetitive peak off-state current:	n=22, C=0
Intermittent operation	AC200V, 2Arms, Ta=25±3℃ For 1min ON, OFF, 500h	USL×2 or less	n=22, C=0
Vibration	200m/s ² 100 to 2000Hz/4min 4times/X, Y, Z direction	(4) On-state voltage: USL×1.2 or less	n=11, C=0
Terminal strength (Bending)	The first bending test is to put back into the original shape after the terminal bent 90° by a 5N load. The second bending test is to do the same but opposite direction. These two tests shall be performed. *1	(5) Minimum trigger current: USL×1.2 or less (6) Isolation resistance, Isolation voltage:	n=11, C=0
Terminal strength (Tension)	Weight: 10N 30s/ terminal direction	Within the value of spec.	n=11, C=0
Soldering heat	260 C, 5s Up to 1.5mm from resin portion *2		n=11, C=0
Solderability	230±5℃, 5±0.5s Use rogin flux. *2	Solder shall adhere at the area or 95% or more of A portion	n=11, C=0

USL: Max. specification values

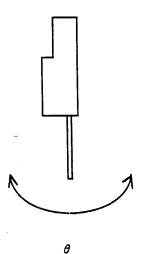
I: Initial values

ED-94045D October 25, 1995

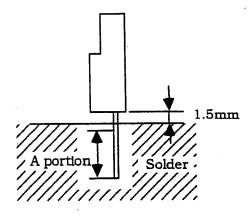
MODEL No. PAGE

\$202TA1 7/9

*1 Terminal bending direction is shown below.



*2 Soldering area is shown below.



A portion: From the lower edge of tiber cut portion to the end of lead

5. Incoming inspection

A single sampling plan, normal inspection level $\, \mathbb{I} \,$ based on ISO 2859 is applied. The AQL according to the inspection items are shown below.

Defect	Inspection items	AQL (%)	Judgement criteria
	Electrical characteristics		
Major defect	Unreadable marking	0.10	Depart on the
	Open, short		Depend on the specification
Minor	Appearance	0.40	
defect	Dimensions	0.40	

Inspection items of electrical characteristics:

 $\mathbf{V_{F}},\,\mathbf{I_{R}},\,\mathbf{I_{DRM}},\,\mathbf{V_{T}},\,\mathbf{I_{H}},\,\mathbf{I_{FT}},\,\mathbf{Viso},\,\mathbf{Riso}$

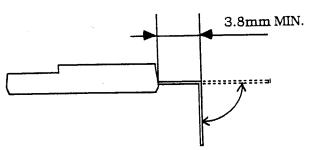
ED-94045D	October 25, 1995
MODEL No.	PAGE
S202T	A1 8/9

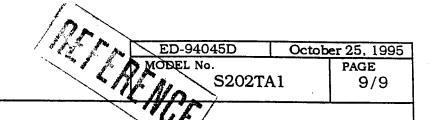
7. Notes

- (1) The LED chip used in the input side of Solid State Relay generally decreases the light emission power after long operation time. The amount of light emission power decrease depends on the ambient temp. and the applied current. (50%/5years) Please decide $I_{\rm F}$ value as 2 times of the Maximum value of the Minimum triggering current at circuit design.
- (2) Please make sure that surge absorption circuit and dv/dt control circuit are provided for protection of \$202TA1. In general, we recommend that both CR circuit and varistor be used in conjunction. Watch for faulty operation that may be caused by leakage current that runs through the CR circuit.
- (3) Current value of the load shall be holded within the range of derating curve. Install an optional heat sink as required.
- (4) The isolation of the coating resin at top of \$202TA1 shall not be guaranteed.
- (5) By using optional heat sink, if it is necessary to take isolation voltage between \$202TA1 and optional heat sink. Please use insulation sheet.
- (6) Optional heat sink shall be installed by screws-fastening torque 0.3 to 0.5N·m. And, please conform to the below items in order to be sunk heat effectively to generating heat in this device.
 - (a) It shall be no unevenness on contacting surface among heat sink, insulation sheet and device.
 - (b) It shall be no burr and metal chip etc. on contacting surface among heat sink, insulation sheet and device.
 - (c) It shall be spread equally silicone grease on contacting surface among heat sink, insulation sheet and device. Silicon grease shall be used such as:
 - ① No secular variation in operating temperature range.
 - ② Base oil does not separate and it does not stay in the device.
 - ③ If base oil permeate into the inside of the device, it does not effect any degradation, for example, due to the expansion of the coating material for chip.

For example, we recommend G-746; Shin-Etsu Chemical Co., Ltd. and SC-102; Dow Corning Toray Silicone Co., Ltd.

- (7) If it is necessary to employ screws with installation of optional heat sink, please solder after fixing screws.
- (8) If it is necessary to bend terminal pins, please bend them 3.8mm or more away from base of terminal pins to prevent mechanical stress between base of terminal pins and resin of mold.





(9) Some have a built-in rectifier such as diode, etc. as part of the electromagnetic counter or solenoid specified for use on AC. If this is the face, check out properly the wave form of the load current. If it is a rectangular wave as it may become, the SSR will not turn OFF.

(10) Cleaning conditions:

1) Solvent cleaning:

Solvent temperature 45°C or less Immersion for 3min or less

2) Ultrasonic cleaning: The affect to device by ultrasonic cleaning is different by cleaning bath size, ultrasonic power output, cleaning time, PWB size or device mounting condition etc. Please test it in actual using condition and confirm that doesn't occur any defect before starting

the ultrasonic cleaning.

3) The cleaning shall be carried out with solvent below.

Solvent:

Ethyl alcohol, Methyl alcohol, Isopropyl alcohol Freon TE • TF, Daiflon-solvent S3-E, S3-MC

Please refrain from using Choloro Fluoro Carbon type solvent to clean devices as much as possible since it is internationally restricted to protect the ozonosphere. Before you use alternative solvent you are requested to confirm that it does not damage package resin.