

PWM/PFM DUAL MODE STEP-DOWN DC/DC CONVERTER

AP1635

Features

- Input voltage range: 2.2V~5V (V_{OUT} type)
- Oscillator frequency: 700KHz (Typ.)
- Internal reference: 1.0V (Typ.)
- High efficiency: 93% (Typ.)
- Current limit and thermal shutdown protection
- Lead Free Package: SOP-8L
- SOP-8L: Available in "Green" Molding Compound (No Br, Sb)
- Lead Free Finish/ RoHS Compliant (Note 1)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definition s/

General Description

The AP1635 series are multi-functional step-down DC/DC converters with built-in speed, low ON resistance drivers. It is capable to deliver more than 1.2A output current with external coil, diode and capacitor.

Output voltage is set-up by the external resistors. (±2.5% accuracy). The 700KHz AP1635 that can work out with small value external components comes out more compact board.

The device switches to and works under PFM mode with light loads. It keeps at high efficiency for both light loads and large output current.

AP1635 can be soft-start with a proper capacitor connected between CE/SS pin and ground. The stand-by current is less than 6uA when CE/SS pin is at "LOW" status. The device is forced to switch off as the voltage at that pin is lower than the stipulated voltage.

Applications

- Electronic Information Organizers
- Palmtops
- Cellular and portable phones
- Portable Audio Systems
- Various Multi-function Power Supplies

Ordering Information



	Device	Package	Packaging	13" Tape and Reel			
	Dornoo	Code	(Note 2)	Quantity	Part Number Suffix		
Pb	AP1635SL-13	S	SOP-8L	2500/Tape & Reel	-13		
	AP1635SG-13	S	SOP-8L	2500/Tape & Reel	-13		

1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at Notes:

http://www.diodes.com/products/lead_free.html. 2. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at $01 \, \mathrm{pc}$



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Pin Assignments



Pin Descriptions

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Pin Name	Pin No.	Description
FB	1	Feedback pin
CE/SS	2	Chip Enable/ Soft Start: H: Enable L: Disable
SVcc	3	IC signal power supply pin, add a 20 Ω resistor to PVcc and a 0.1µF capacitor to GND.
PVcc	4	IC power supply pin
SW	5/6	Switch Pin. Connect external inductor/diode here. Minimize trace area at this pin to reduce EMI.
GND	7/8	GND Pin



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Block Diagram



Absolute Maximum Ratings (T_A=25°C)

Symbol	Parameter	Ratings	Units
V_{cc}/SV_{cc}	V _{IN} Pin Voltage	-0.3 ~ 5.0	V
V _{SW}	SW Pin Voltage	-0.3 ~ V _{IN} +0.3	V
V _{FB}	FB Pin Voltage	-0.3 ~ V _{IN} +0.3	V
V _{CE/SS}	CE/SS Pin Voltage	-0.3 ~ V _{IN} +0.3	V
PD	Continuous Total Power Dissipation	Internal limited	
T _{OPR}	Operating Ambient Temperature	-25 ~ +80	°C
T _{STG}	Storage Temperature	-40 ~ +125	°C



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Electrical Characteristics

V_{IN}=5V, V_{OUT}=2V, Load=300mA, TA=25°C

SymbolParameterConditionsMinTyp.MaxUnits V_{FB} FB0.9751.01.025V V_{IN} Input Voltage2.2-5VLine Regulation V_{IN} =2.2~5V, Load=10mA0.12%Load Regulation I_{OUT} =10~1200mA1.2% V_{UVLO} UVLO Voltage (min. operating voltage) V_{CC} , voltage required to maintain H at V_{OUT} 1.2% I_{CC} Operating CurrentCE/SS=V_IN, No Load-100150 μ A I_{CC} Supply CurrentNo external components, CE/SS=V_IN, VFB=1.2V-90120 μ A I_{STB} Stand-by CurrentNo external components, CE/SS=0V, VFB=0V-6- μ A I_{CL} Current LimitPeak current V_{IN} =5V, V_{OUT} =2V500700-KHzMAXDTYMaximum Duty RatioNo load152535% V_{CEH} CE/SS "High" VoltageApply 1.4V (min.) to CE/SS, determine V_{OUT} "High"1.4V V_{CEH} CE/SS "Low" VoltageSame as V_{CEH} , determine V_{OUT} "Low"-0.6VFFIEfficiency V_{CC} =5V, V_{OUT} =3.3V, Load=300mA-93-%RedsonRdson Condition I_{OUT} =300mA, V_{IN} =5V, V_{OUT} =2V-350450mΩ									
$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	Symbol	Parameter	Conditions	Min	Тур.	Max	Units		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	V_{FB}	FB		0.975	1.0	1.025	V		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	V _{IN}	Input Voltage		2.2	-	5	V		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		Line Regulation	V _{IN} =2.2~5V, Load=10mA	-	-	0.12	%		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		Load Regulation	I _{OUT} =10~1200mA	-	-	1.2	%		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	V	UVLO Voltage (min.	\mathcal{M} voltage required to maintain \mathcal{H} at \mathcal{M}	-	-	2	V		
$\begin{array}{c cccc} I_{CCQ} & Supply Current & No external components, \\ CE/SS=V_{IN}, V_{FB}=1.2V & - 90 & 120 & \mu A \\ \hline I_{STB} & Stand-by Current & No external components, \\ CE/SS=0V, V_{FB}=0V & - 6 & - & \mu A \\ \hline I_{CL} & Current Limit & Peak current \\ V_{IN}=5V, V_{OUT}=2V & 1200 & 1400 & 1600 & mA \\ \hline Fosc & Oscillator Frequency & Load=300mA, V_{IN}=5V, V_{OUT}=2V & 500 & 700 & - & kHz \\ \hline MAXDTY & Maximum Duty Ratio & 85 & 90 & - & \% \\ \hline PFMDTY & PFM Duty Ratio & No load & 15 & 25 & 35 & \% \\ \hline V_{CEH} & CE/SS "High" Voltage & Apply 1.4V (min.) to CE/SS, determine \\ \hline V_{CEL} & CE/SS "Low" Voltage & Same as V_{CEH}, determine V_{OUT}/"Low" & - & 0.6 & V \\ \hline EFFI & Efficiency & V_{CC}=5V, V_{OUT}=3.3V, Load=300mA & - & 93 & - & \% \\ \hline \end{array}$	VUVLO	operating voltage)	v_{CC} , voltage required to maintain H at v_{OUT}						
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$\begin{array}{c c c c c c c c c c c c c c c c c c c $	I _{CCQ}	Supply Current		-	90	120	μA		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $									
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	I _{STB}	Stand-by Current		-	6	-	μA		
OL V_{IN} =5V, V_{OUT} =2V 500 700 - kHz Fosc Oscillator Frequency Load=300mA, V_{IN} =5V, V_{OUT} =2V 500 700 - kHz MAXDTY Maximum Duty Ratio 85 90 - % PFMDTY PFM Duty Ratio No load 15 25 35 % V_{CEH CE/SS "High" Voltage Apply 1.4V (min.) to CE/SS, determine V_{OUT} "High" 1.4 - - V V_{CEL CE/SS "Low" Voltage Same as V_{CEH} , determine V_{OUT} "Low" - - 0.6 V EFFI Efficiency V_{cc} =5V, V_{OUT} =3.3V, Load=300mA - 93 - %				1200	1400	1600	mA		
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$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	MAXDTY	Maximum Duty Ratio		85	90	-	%		
V_{CEH} CE/SS High Voltage V_{OUT} "High"1.4V V_{CEL} CE/SS "Low" VoltageSame as V_{CEH} , determine V_{OUT} "Low"0.6VEFFIEfficiency V_{CC} =5V, V_{OUT} =3.3V, Load=300mA-93-%	PFMDTY	PFM Duty Ratio	No load	15	25	35	%		
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	V _{CEH}	CE/SS "High" Voltage		1.4	-	-	V		
EFFI Efficiency V _{CC} =5V, V _{OUT} =3.3V, Load=300mA - 93 - %	V _{CEI}	CE/SS "Low" Voltage		-	-	0.6	V		
				-	93	-	%		
	Rdson	Rdson Condition		-	350	450	mΩ		



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CONVERTER

Typical Performance Characteristics



AP1635 Rev. 5



PWM/PFM DUAL MODE STEP-DOWN DC/DC CONVERTER

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Typical Performance Characteristics (Continued)



Typical Application Circuit





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Marking Information

(1) SOP-8L



Package Information (All Dimensions in mm)

(1) Package Type: SOP-8L





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