



Parameter	Ratings	Units
Blocking Voltage	350	V _P
Load Current	120	mA _{rms} & mA _{DC}
On-Resistance (max)	35	Ω

Features

- Built-in Current Limiting
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 6-Pin Package
- Flammability Rating UL 94 V-0
- Surface Mount, Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

LCA110L is a current-limited, normally open (1-Form-A) solid state relay that uses optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation.

Its optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

Use the LCA110L to replace mechanical relays, and gain the superior reliability associated with semiconductor devices, which, because they have no moving parts, can offer faster, bounce-free switching in a more compact surface mount or through-hole package.

Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: Certificate available on our website

Ordering Information

Part #	Description
LCA110L	6 Pin DIP (50/Tube)
LCA110LS	6 Pin Surface Mount (50/Tube)
LCA110LSTR	6 Pin Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices







Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	350	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	Α
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics		1			I	
Load Current, Continuous						
AC/DC Configuration		I			120	mA _{rms} & mA _D
DC Configuration	-	Ľ	-	-	200	mA _{DC}
Load Current Limiting, AC/DC Configuration	I _F =2mA, V _L =8V	I _{CL}	±130	±170	±210	mA
On-Resistance ¹						
AC/DC Configuration	I _L =120mA	Р		19	35	Ω
DC Configuration	I_=200mA	R _{ON}	-	7	10	52
Off-State Leakage Current	V _L =350V _P	ILEAK	-	-	1	μA
Switching Speeds						
Turn-On	L_EmA_\/_10\/	t _{on}		-	3	
Turn-Off	I _F =5mA, V _L =10V	t _{off}	-	-	3	ms
Output Capacitance	I _F =0mA, V _L =50V, f=1MHz	C _{OUT}	-	25	-	pF
Input Characteristics						1
Input Control Current to Activate	I _L =120mA	۱ _۶	-	1.1	2	mA
Input Control Current to Deactivate	-	I _F	0.4	1.0	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.5	V
Reverse Input Current	V _R =5V	I _B	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	V _{IO} =0V, f=1MHz	CIO	-	3	-	pF

asurement taken within 1 second of on-time.



LCA110L

PERFORMANCE DATA*







Typical I_c for Switch Operation (N=50, I, =120mA_{DC}) 25 20 Device Count (N) 15 10 5 0 0.45 0.75 1.05 1.35 1.65 1.95 2.25 LED Current (mA)









Typical Blocking Voltage Distribution (N=50)





*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C. For guaranteed parameters not indicated in the written specifications, please contact our application department.



LCA110L

PERFORMANCE DATA*



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Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification		
LCA110L / LCA110LS	MSL 1		

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be $(T_C - 5)^{\circ}C$ or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature (T _c)	Dwell Time (t _p)	Max Reflow Cycles
LCA110L	250°C	30 seconds	1
LCA110LS	250°C	30 seconds	3

Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.



R05



MECHANICAL DIMENSIONS

LCA110L



PCB Hole Pattern



Dimensions mm (inches)

LCA110LS









Dimensions mm (inches)



LCA110L

LCA110LSTR Tape & Reel



MECHANICAL DIMENSIONS

For additional information please visit our website at: www.ixysic.com

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