	per:	20131209002 PC						CN Date:	12/27/2013		
Title:	Qualification of RFAB and JCAP as additional Fab site and Assembly/Test site options for select devices in the LBC8LV Fab process.										
Customer	PCN /	Mana	ager	Phor	ne: +1(21	4)480-603	7 Dept: Quality Services				
*Proposed 1 st Ship Date		te:	03/27/20		2014	Estimato Availabi	ated Sample			Provided at ple request	
Change Ty									1		
						Process			Assembly		
Design								nical Specification			
Test Site				Packing/Shipping/Labeling					Test Proce		
Wafer Bump Site Wafer Bump Material Wafer Bump Wafer Fab Site Wafer Fab Materials Wafer Fab Fab Fab											
						per change				riocess	
						N Detail					
Descriptio	on of Change				FC	N Detan	3				
Group 1 (I	with current t F ab Site): ite/Process/W						e/Process/W	/afer	Diameter]	
UMC-F8	E/LBC8LV Pro	cess/2	200	mm		RFAB/LBC	8LV Proces	s/30)0mm		
	process was q 13. Qualifica	ualifie	ed a	t RF/	AB on 2/				<u>JCP</u> kage was qu	」 Jualified at JCAP	
Reason fo	r Change:										
Continuity											
		F				Quality	en Delleki	14		n a nativa).	
Anticipate	ed impact on	Forn	n, F	π, F	unction	i, Quality	or Reliable	ιτy (positive /	negative):	
	o product id	entifi	icat	tion	resultir	ng from th	is PCN:				
Changes t	o product id	entifi	icat	tion	resultir	ng from th	is PCN:				
Changes t Fab Site						-			de (21 I)	7	
Changes t Fab Site Current Cl			te C	Code	(20L)	-	hip Countr		de (21 L)		
Changes t Fab Site Current Cl UMC-F8E	hip Site C	hip Si	te C	Code CSO:	(20L) U8E	- (hip Countr	WN			
Changes t Fab Site Current Cl	hip Site C	hip Si	te (te (Code CSO: Code	(20L)	- (hip Countr 1 hip Countr	WN			
Changes t Fab Site Current Cl UMC-F8E New Chip RFAB	hip Site C	hip Si	te (te (Code CSO: Code	(20L) U8E (20L)	- (hip Countr 1 hip Countr	WN y Co			
Changes t Fab Site Current Cl UMC-F8E New Chip RFAB Assembly	hip Site C Site C	hip Si	te (te (Code CSO: Code	(20L) U8E (20L)	- (hip Countr 1 hip Countr	WN y Co			
Changes t Fab Site Current Cl UMC-F8E New Chip RFAB Assembly Current As	hip Site C	hip Si	te C	Code CSO: Code CSO:	(20L) U8E (20L) RFB		hip Countr 1 hip Countr	WN y Co JSA	de (21 L)		
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The symbolization will ASSEMBLY SITE CODE				P						
Sample Product Ship	oping l	.abel	(not actual	produ	uct label)					
TEXAS INSTRUMENTS MADE IN: Malaysia 2DC: 20: MSL 2 /260C/1 YEAR MSL 1 /235C/UNLIM OPT: ITEM: LBL: 5A (L)TO	SEAL D 03/29/0 39	24		11月1日の日本には 11月1日日日本には 11月1日日本には 11月1日本には 11月1日本には 11月1日本には 11月1日本には 11月11日年には 11月11日年には 11月11日年には 11月11日日本には 11月11日年には 11月11日年には 11月11日年には 11月11日日本には 11月	LOL OGOIONE ((D) 033	A SI2 17 ISA			
Product Affected Gro	1		-							
DRV2604YZFR		604YZ			2605YZFR	DRV2605Y	DRV2605YZFT			
Product Affected Gro	oup 2 (Asse	mbly and T	est S	ite):					
DRV2604YZFR DRV2604YZF			FT DRV2605YZFR			DRV2605Y	DRV2605YZFT			
This qualification has by validate that the property of the pr	een de sed ch	velope ange i	ed for the va neets the ap	lidati plica		The qualifical specifie	fication dations.	ata will		
Wafer Fal		RFAE			•	_BC8				
Wafer Diar						••				
Qualification:	lan	🛛 Те	st Results							
Reliability Test			Conditions			Sample Size / Fail Lot#1 Lot#2 Lot#3				
Life Test			125C (1000 hours)			77/0	76/0	71/0		
Electrical Characterization			Per datasheet specification			Pass	-	-		
**Biased HAST			130C/85%RH (96 hours)			77/0	77/0	77/0		
**Autoclave			121C (96 hours)			77/0	77/0	77/0		
**Temp Cycle			-65/150C (500 cycles)			77/0	77/0	77/0		
**High Temp Storage Bake			170C (420 hours)			77/0 5/0	77/0	77/0		
				Per datasheet			5/0	5/0		
ESD CDM			Dor datach	oot		5/0	5/0	5/0		
ESD CDM ESD HBM Latch-up			Per datash Per JESD78			5/0 6/0	5/0 6/0	5/0 6/0		

Qualification Data Group 2: Approved 2/19/2013							
This qualification has been developed for the validation of this change. The qualification data will						ıta will	
validate that the proposed change meets the applicable released technical specifications.							
Qualification Device 1: CD3230A0YFF (MSL LEVEL1-260C)							
Assembly Site: JCAP		Bump Composition:		SnAgCu			
# Pins-Designator, Family:	130-YFF, WCSP		Bump Diameter:	0.25mm			
Qualification: 🗌 Plan 🛛 Test Results							
Reliability Test		Conditions		Sample Size / Fail			
				Lot#1	Lot#2	Lot#3	
**Unbiased HAST		130C/85%RH (96 hours)		85/0	82/0	78/0	
**High Temp Storage Bake	170C (420 hours)		84/0	80/0	78/0		
**Biased HAST	130C/85%RH (96 hours)		85/0	-	-		
Life Test	125C (1000 hours)		80/0	-	-		
**Temp Cycle	-55/125C (1000 cycles)		85/0	85/0	78/0		
**Preconditioning: Level 1-260C							

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com