

Advance Product Change Notification

Issue Date: 14-Feb-2016

Here's your personalized quality information concerning products Digi-Key purchased from NXP. For detailed information we invite you to view this notification online

201601011A



Change Category

[] Wafer Fab process[] Assembly Process [] Product Marking [] D

[] Wafer Fab [X] Assembly [] Electrical spec./Test [] Mechanical Specification

materials Materials coverage

[] Wafer Fab location[] Assembly Location [] Test Location [] Packing/Shipping/Labeling

Change of bond wire and new 2nd source mold compound in DFN2020-3 and DFN2020-6

Details of this Planned Change

Scheduled changes affect transistors in DFN2020-3 (SOT1061) and DFN2020-6 (SOT1118) packages.

- (1) The bond wire material will be changed from gold to copper. Gold wire remains qualified for supply security reasons only.
- (2) A second source mold compound supplier will be introduced.

Old product: Wire material is gold (Au) (with currently used mold compound suppliers)
Changed product: Wire material is copper (Cu) or gold (Au) (with currently used first and new second source mold compound supplier)

The design and materials of all other components will remain unchanged: die, die attach, and lead frame.

Reliability qualification and full electrical characterization over temperature will be performed. For details please see the Self Qualification Plan.

No change on thermal behavior or mechanical dimensions. Electrical parameters will remain unchanged (in specification and with the same distribution).

Why do we Plan this Change

- (1) Aligning with world technology standards, NXP continues to introduce copper wire for plastic SMD packages. Copper wire shows enhanced mechanical properties.
- (2) Following NXP company policy of second source material availability, a second source mold compound will be added to the BOM package. The second source supplier is already well established for NXP

semiconductor products.

Identification of Affected Products

Changed products can be identified by date code after implementation.

Product Availability

Sample Information

Samples are available upon request

Samples can be ordered now and will be shipped after FPCN issue date.

Production

Planned first shipment 20-May-2016

Impact

No impact to the products' functionality anticipated.

Disposition of Old Products

Existing inventory will be shipped until depleted

Timing and Logistics

The Self Qualification Report will be ready on 05-Apr-2016.

The Final PCN is planned to be issued on: 05-Apr-2016.

Your acknowledgement of this change, conform JEDEC JESD46 D, is expected till 15-Mar-2016.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name GA Customer Support

e-mail address DiscrQA.Helpdesk.GA-Products@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

You have received this email because you are a designated contact or subscribed to NXP's Quallity Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

NXP | Privacy Policy | Terms of Use

NXP Semiconductors

High Tech Campus, 5656 AG Eindhoven, The Netherlands

 $\hfill @$ 2006-2010 NXP Semiconductors. All rights reserved.