

# **3 V SILICON RFIC** FREQUENCY UPCONVERTER

## **UPC8163TB**

#### FEATURES

- **RECOMMENDED OPERATING FREQUENCY:** frFOUT = 0.8 GHz to 2.0 GHz fIFIN = 50 MHz to 300 MHz
- SUPPLY VOLTAGE: Vcc = 2.7 to 3.3 V
- HIGH DENSITY SURFACE MOUNTING: 6-pin super minimold package
- HIGH IP3: OIP3 = +9.5 dBm @ frfout = 900 MHz
- MINIMIZED CARRIER LEAKAGE: Due to double balanced mixer

#### DESCRIPTION

NEC's UPC8163TB is a silicon RFIC designed as a frequency upconverter for cellular/cordless telephone transmitter stages, and features improved intermodulation. This device is housed in a 6 pin super mini mold or SOT-363 package making it ideal for reducing system size. The UPC8106TB is manufactured using NEC's 20 GHz fT NESAT<sup>TM</sup> III silicon bipolar process.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

## **BLOCK DIAGRAM**



#### **APPLICATIONS**

Digital Cellular/Cordless Phones

#### **ELECTRICAL CHARACTERISTICS**

(TA = 25°C, Vcc = VRFOUT = 3.0 V, fifin = 240 MHz, PLOIN = -5 dBm unless otherwise specified)

PART NUMBER PACKAGE OUTLINE				UPC8163TB S06		
SYMBOLS	PARAN	ETERS AND CONDITIONS	UNITS	MIN	ТҮР	MAX
lcc	Circuit Current (no signal) mA				16.5	23
CG	Conversion Gain	frequt = 0.9 GHz, Pifin = -30 dBm frequt = 1.9 GHz, Pifin = -30 dBm	dB dB	6 4	9 7	12 10
PSAT	Saturated Output Power	frfout = 0.9 GHz frfout = 1.9 GHz	dBm dBm	-1.5 -4.5	0.5 -2	
OIP3	Output third Order Intercept Point, fiFIN1 = 240 MHz free   fiFIN2 = 240.4 MHz fRFOUT = 0.9 GHz   PIFIN = -20 dBm fRFOUT = 1.9 GHz		dBm dBm		+9.5 +6.0	
NF	SSB Noise Figure	frfout = 0.9 GHz frfout = 1.9 GHz	dB dB		12.5 12.5	

## ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage <sup>2</sup>	V	3.6
Рт	Total Power Dissipation <sup>3</sup>	mW	200
Pin	Input Power	dBm	+10
Тор	Operating Temperature	°C	-40 to +85
Тѕтс	Storage Temperature	°C	-55 to +150

Notes:

1. Operation in excess of any one of these conditions may result in permanent damage.

2.  $T_A = 25^{\circ}C$ , pins 5 and 6.

Mounted on a double-sided copperclad 50x50x1.6 mm epoxy glass PWB, TA = 85°C).

#### RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage <sup>1</sup>	V	2.7	3.0	3.3
PLOIN	Local Input Level <sup>2</sup>	dBm	-10	-5	0
<b>f</b> RFOUT	RF Output Frequency <sup>3</sup>	GHz	0.8	_	2.0
fifin	IF Input Frequency	MHz	50	-	300
Тор	Operating Temperature	°C	-40	+25	+85

Notes:

1. Same voltage applied to pins 5 and 6

2.  $ZS = 50 \Omega$  (without matching)

3. With external matching circuit

#### **PIN FUNCTIONS**

Pin No.	Pin Name	Applied Voltage	Pin Voltage	Description	Equivalent Circuit	
1	IFINPUT	_	1.2	This pin is the IF input to double balanced mixer. The input is a high impedance.		
2 4	GND	0	_	GND pin. Ground pattern on the board should be as wide as possible. Trace length should be kept as short as possible to minimize ground impedance.		
3	LOINPUT	_	2.1	LO input pin. Recommended input level is -10 to 0 dBm.		
5	Vcc	2.7 to 3.3	_	Supply voltage pin.	│	
6	RFoutput	Same bias as Vcc through external inductor	_	This pin is the RF output. This pin is designed as an open collector. Due to the high impedance output, this pin requires an external LC matching circuit.		

## **APPLICATION EXAMPLE**



#### OUTLINE DIMENSIONS (Units in mm)

#### PACKAGE OUTLINE S06



Note: All dimensions are typical unless otherwise specified.

## LEAD CONNECTIONS

(Top View) (Bottom View) 3 2 3 4 4 4 4 5 5 5 2 1 6 6 0 1

- 1. IF INPUT
- 2. GND
- 3. LO INPUT
- 4. GND
- 5. Vcc
- 6. RF OUTPUT

#### **ORDERING INFORMATION**

PART NUMBER	QUANTITY
UPC8163TB-E3-A	3K/Reel

Note: Embossed tape 8 mm wide. Pins 1,2,3 face tape perforation side.

#### TEST CIRCUIT 1 (RFOUT = 900 MHz)



#### TEST CIRCUIT 2 (RFOUT = 1.9 GHz)



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РВВ	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not De	etected

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