

2108311118 EFP01 New Revision E Parts and Datasheet v1.3 Available

PRCN Issue Date: Aug 31, 2021

Effective Date: Dec 06, 2021

PCN Type: Datasheet; Product Revision

Description of Change

Silicon Labs is pleased to announce a new revision of EFP01. This revision was made to improve yields and help ensure continuity of supply.

In addition to the new revision, several new parts are being introduced.

New part numbers:	
EFP0101GM20-E	EFP0101 Wired Buck PMIC
EFP0102GM20-E	EFP0102 Wired Buck PMIC
EFP0103GM20-E	EFP0103 Wired Buck with LDO PMIC
EFP0106GM20-E	EFP0106 Buck/Boost PMIC

The datasheet is being updated to revision 1.3 to add information on the new part numbers. See the datasheet revision history for a complete list of changes.

Note: After the effective date of the PRCN, Silicon Labs reserves the right not to accept orders for the old revision.

Reason for Change

IC revision to improve yields to improve continuity of supply.

Impact on Form, Fit, Function, Quality, Reliability

No impact on Form, Fit, Function, Quality or Reliability.

Product Identification

Existing Part #	Replacement Part #	DropInCompInd
EFP0104GM20-D	EFP0104GM20-E	Yes
EFP0104GM20-DR	EFP0104GM20-ER	Yes
EFP0107GM20-D	EFP0107GM20-E	Yes
EFP0107GM20-DR	EFP0107GM20-ER	Yes
EFP0108GM20-D	EFP0108GM20-E	Yes
EFP0108GM20-DR	EFP0108GM20-ER	Yes
EFP0109GM20-D	EFP0109GM20-E	Yes
EFP0109GM20-DR	EFP0109GM20-ER	Yes
EFP0110GM20-D	EFP0110GM20-E	Yes
EFP0110GM20-DR	EFP0110GM20-ER	Yes
EFP0111GM20-D	EFP0111GM20-E	Yes
EFP0111GM20-DR	EFP0111GM20-ER	Yes

Kit Identification

Kits impacted by the above product are listed below. Orders for the following obsolete kits will no longer be accepted.

Existing Kit # Replacement Kit # NA NA

Last Date of Unchanged Product: Dec 06, 2021

Qualification Samples

Available upon request.

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <u>http://www.silabs.com/profile</u>

Qualification Data

See attached.

			Lot ID or	Fail/Pass or			
Test Name	Test Condition	Qualification	Start	End	Notes	Summary	Status
Test Group A – Accel	lerated Environment Stress Test	s		8		s. 17.	с.
HAST	JA110		Q044904	0/26	3		
	130°C, 85%RH	3 lots, N=>25	Q045202	0/28	3	3 lots	Pass
	Vcc=5.5V, 96 hours		Q043693	0/27	2	0/81	
Temp Cycle	JA104		Q048180	0/28			
	Cond C: -65°C to 150°C	3 lots, N=>25	Q044626	0/28	3	3 lots	Pass
	500 cycles		Q045205	0/28	3	0/84	
ITSL	JA103		Q044625	0/28	3		
	150°C, 1000hr	3 lots, N=>25	Q045204	0/28	3	3 lots	Pass
			Q043485	0/28	2	0/84	
reconditioning	JESD22-A113		Q048179	0/80	1		
	MSL2 @ 260°C	3 lots, N=>75	Q044796	0/123	1, 3	3 lots	Pass
	85°C/60%RH / 168hr		Q045126	0/130	1, 3	0/333	
JHAST	JESD22-A118		Q043894	0/27	2		
	Cond A - 130°C / 85%RH / 96h	3 lots, N=>25	Q045203	0/28	3	3 lots	Pass
			Q044627	0/28	3	0/83	
Fest Group B - Acce	lerated Lifetime Simulation Test	s					
ITOL	JA108		Q045293	0/82	3		
	T _J ≥ 125°C, Dynamic	3 lots, N=>77	Q047858	0/85		3 lots	Pass
	Vcc=5.5V, 1000 hours		Q044635	0/85	3	0/252	
TOL	JA108	1	Q043483	0/36	2		
	T _A = -10°C, Dynamic	1 lot, N=>32				1 lots	Pass
	Vcc=5.5V, 1000 hours					0/36	
ELFR	JA108		Q044636	0/503	3		
	T _J ≥ 125°C, Dynamic	2 lots, N=>500	Q045549	0/504	4	2 lots	Pass
	Vcc=5.5V, 48 hours		0.0000000000000000000000000000000000000			0/1007	
Data Retention High	JESD22-A117+JESD22-A103		Q045207	0/42	3		
ſemp	150°C / 1000hr	3 lots, N =>39	Q044620	0/42	3	3 lots	Pass
			Q043691	0/39	2	0/123	
est Group E - Elect	rical Verification						
SD-HBM	JS-001	1	1			<u> </u>	
	00-001	1 lot Marsh	Q048183			2 kV	Class
		1 lot, N=>3	Q048183			2 KV	Class 2

EFP01xxGM20-E Qualification Report

Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
ESD-CDM	JS-002	1 lot, N=>3	Q048181			TC 550	Class C2a
Latch Up	JESD78 ±100mA Overvoltage = 5.5V	1 lot, N=>3 1 lot, N=>3	100000000000000000000000000000000000000	25 °C 130 °C	Class I Class II	±200mA ±200mA	Pass
Notes: (OP01858)							
Notes: (QP01858) 1. Parts are Pre-co 2. QBS to QP0158 3. QBS to QP0168 4. QBS to QP0175	9						
1. Parts are Pre-co 2. QBS to QP0158 3. QBS to QP0168 4. QBS to QP0175	8 9 8 This re	eport applies to the fo					
1. Parts are Pre-co 2. QBS to QP0158 3. QBS to QP0168 4. QBS to QP0175 EFP0101GM20-	8 9 8 This r E EFP0101GM20-ER	eport applies to the fo	EFP01	07GM20-E	100	FP0107GM2	
1. Parts are Pre-co 2. QBS to QP0158 3. QBS to QP0168 4. QBS to QP0175 EFP0101GM20- EFP0102GM20-	8 9 8 E EFP0101GM20-ER E EFP0102GM20-ER	eport applies to the fo	EFP01 EFP01	07GM20-E 08GM20-E	E	FP0108GM2	0-ER
1. Parts are Pre-co 2. QBS to QP0158 3. QBS to QP0168 4. QBS to QP0175 EFP0101GM20-	8 9 8 E EFP0101GM20-ER E EFP0102GM20-ER E EFP0103GM20-ER	eport applies to the fo	EFP01 EFP01 EFP01	07GM20-E	E		0-ER

EFP01xxGM20-E Qualification Report



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized to key products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOmodem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc. 400 West Cesar Chavez Austin, TX 78701

http://www.silabs.com