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|--------------------------|-----------------------------|-------------------------------------|---------------------|
| PCN Number: | 20181126000.0 | PCN Date: | Dec. 19, 2018 |
| Title: | Datasheet for DS90UB914A-Q1 | | |
| Customer Contact: | PCN Manager | Dept: | Quality Services |
| Change Type: | | | |
| <input type="checkbox"/> | Assembly Site | <input type="checkbox"/> | Design |
| <input type="checkbox"/> | Assembly Process | <input checked="" type="checkbox"/> | Data Sheet |
| <input type="checkbox"/> | Assembly Materials | <input type="checkbox"/> | Part number change |
| <input type="checkbox"/> | Mechanical Specification | <input type="checkbox"/> | Test Site |
| <input type="checkbox"/> | Packing/Shipping/Labeling | <input type="checkbox"/> | Test Process |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Bump Site |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Bump Material |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Bump Process |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Fab Site |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Fab Materials |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Fab Process |

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



DS90UB914A-Q1

SNLS499C –APRIL 2016–REVISED NOVEMBER 2018

Changes from Revision B (October 2016) to Revision C

Page

| | |
|---|----|
| • Clarified when PCLK becomes active with respect to LOCK | 6 |
| • Added Power Over Coax supply noise to the recommended operating conditions table | 9 |
| • Corrected to t_{DLH} and t_{DHL} for data low-to-high and high-to-low transition time | 15 |
| • Moved the timing diagrams to the <i>Parameter Measurement Information</i> section | 17 |
| • Added reference to compatibility with DS90UB953-Q1/935-Q1 serializers | 21 |
| • Updated pullup and pulldown resistor to R_1 and R_2 in MODE pin configuration diagram | 29 |
| • Updated register "TYPE" column per legend | 38 |
| • Added type and default value to the reserved register bits that were missing this information | 38 |

The datasheet number will be changing.

| | | |
|----------------------|--------------|------------|
| Device Family | Change From: | Change To: |
| DS90UB914A-Q1 | SNLS499B | SNLS499C |

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/DS90UB914A-Q1>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

| | | | |
|-------------------|-------------------|------------------|--|
| DS90UB914ATRHSJQ1 | DS90UB914ATRHSRQ1 | DS90UB914ATRHSQ1 | |
|-------------------|-------------------|------------------|--|

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

| Location | E-Mail |
|--------------|--|
| USA | PCNAmericasContact@list.ti.com |
| Europe | PCNEuropeContact@list.ti.com |
| Asia Pacific | PCNAsiaContact@list.ti.com |
| Japan | PCNJapanContact@list.ti.com |